



Modelling CoCoME with DisCComp

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Sebastian Herold

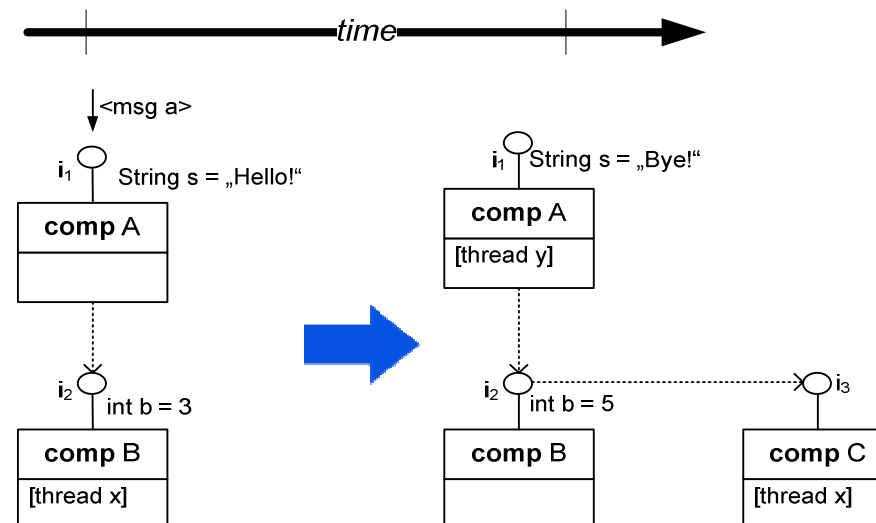
Clausthal University of Technology
Department of Informatics – Software Systems Engineering
Chair of Prof. Dr. Andreas Rausch

- Introduction
 - The team
- The DisCComp Approach
 - History of DisCComp and motivation for participating in the contest
 - Foundations of the system model (formal semantics)
 - Foundations of the specification technique
- The Modelled CoCoME Cutout
 - Static view
 - Behavioural view
- Conclusion
 - Experiences, limitations
 - Future work

- **Affiliation**
 - TU Clausthal, Software Systems Engineering Group (formerly known as Software Architecture Group from Kaiserslautern)
- **Members**
 - André Appel, Holger Klus, Andreas Rausch, Sebastian Herold
- **Component Approach**
 - DisCComp: A Formal Model for Distributed Concurrent Components
- **Specification Technique**
 - UML-based, OCL-based
- **Experiences**
 - Seamless UML software/system modeling
 - Software architecture in general

The DisCComp Approach

- DisCComp: set-theoretic formalization of distributed concurrent components which allows
 - synchronous and asynchronous messages
 - a shared global state
 - dynamically changing structures



- Instances in a system s :

$\text{Instance}_s := \text{System}_s \cup \text{Component}_s \cup \text{Interface}_s \cup \text{Attribute}_s \cup \text{Connection}_s \cup$
 $\text{Message}_s \cup \text{Call}_s \cup \text{Thread}_s \cup \text{Value}_s$

- The system state

- Structural state

$\text{alive}_s := \text{Instance}_s \rightarrow \text{BOOLEAN}$

$\text{assignment}_s := \text{Interface}_s \rightarrow \text{Component}_s$

$\text{allocation}_s := \text{Attribute}_s \rightarrow \text{Interface}_s$

$\text{connects}_s := \text{Connection}_s \rightarrow \{(\text{from}, \text{to}) \mid \text{from} \in \text{Component}_s \cup \text{Interface}_s, \text{to} \in \text{Interface}_s\}$

- Valuation state

$\text{valuation}_s := \text{Attribute}_s \rightarrow \text{Value}_s$

- The system state

- Communication state

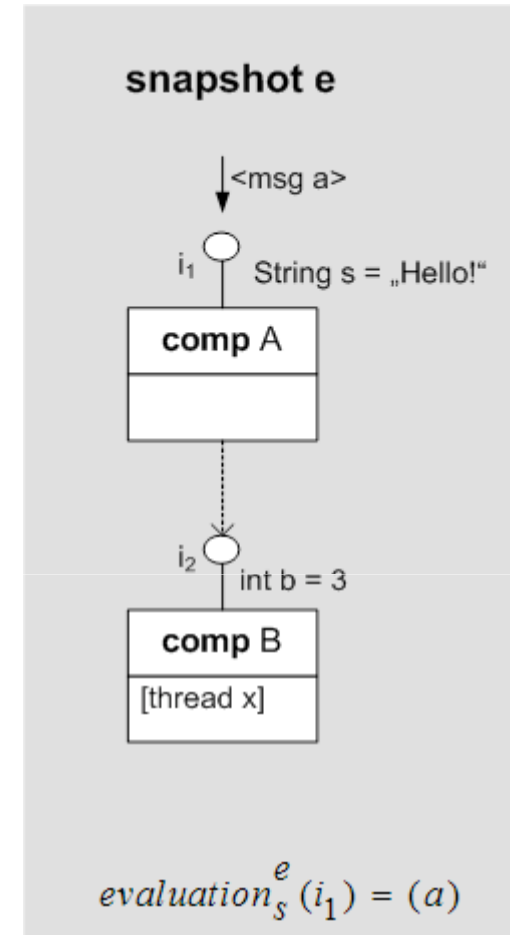
$$\text{evaluation}_s : \text{Interface}_s \rightarrow \text{Message}_s^*$$

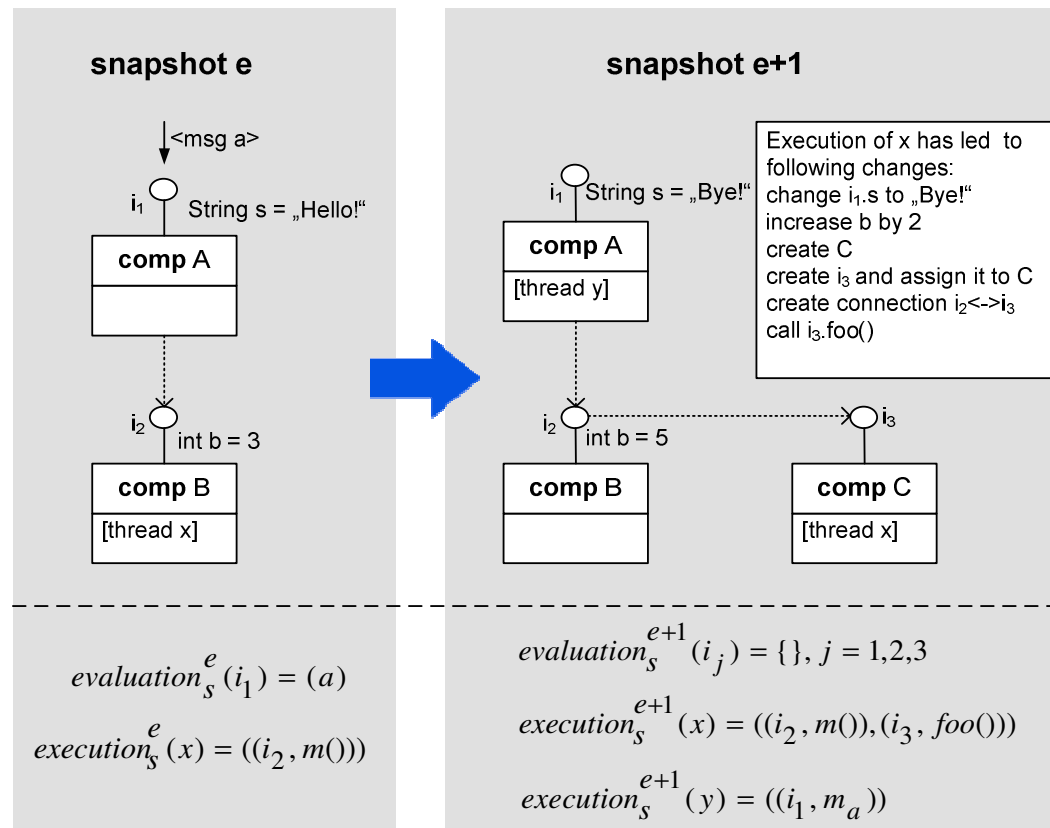
- Execution state

$$\text{execution}_s : \text{Thread}_s \rightarrow (\text{Interface}_s \times \text{Call}_s)^*$$

- The system's overall state at some point in time e is denoted as *snapshot*:

$$\text{snapshot}_s^e : \text{alive}_s^e \times \text{assignment}_s^e \times \text{allocation}_s^e \times \text{connects}_s^e \times \text{valuation}_s^e \times \text{evaluation}_s^e \times \text{execution}_s^e$$





- A thread is selected for execution (runtime environment).
- Pending asynchronous messages are processed, threads are created.
- Changes, the threads requires, are computed by:

$$\text{behaviour}_s : \text{Thread}_s \times \text{Snapshot}_s \rightarrow \text{Snapshot}_s$$

- Operator to replace elements in sets (relations):

$$X \triangleleft Y := \{a \mid a \in Y \vee (a \in X \wedge \pi_1(\{a\}) \cap \pi_1(Y) = \{\})\}$$

- Composing the system behaviour (=computing the next snapshot)

$next_snapshot(snapshot_s^e) := snapshot_s^{e+1} = (alive_s^{e+1}, assignment_s^{e+1}, \dots)$ with

$alive_s^{e+1} = alive_s^e \triangleleft (\pi_1(behaviour(snapshot_s^e, next_thread())) \triangleleft \pi_1(message_execution(snapshot_s^e)))$

$assignment_s^{e+1} = assignment_s^e \triangleleft \pi_2(behaviour(snapshot_s^e, next_thread()))$

$allocation_s^{e+1} = allocation_s^e \triangleleft \pi_3(behaviour(snapshot_s^e, next_thread()))$

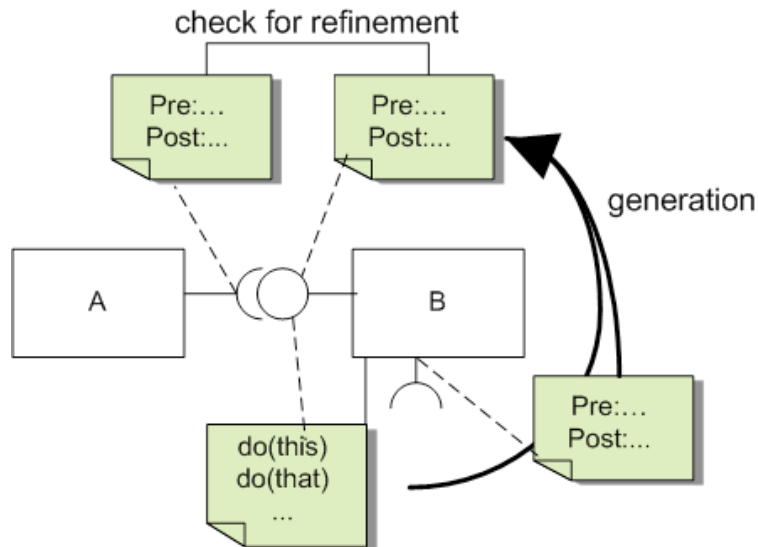
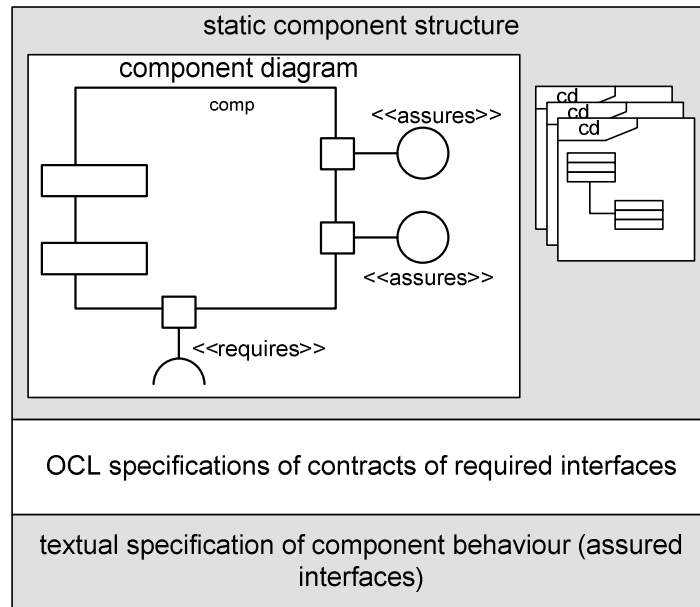
$connects_s^{e+1} = connects_s^e \triangleleft \pi_4(behaviour(snapshot_s^e, next_thread()))$

$valuation_s^{e+1} = valuation_s^e \triangleleft \pi_5(behaviour(snapshot_s^e, next_thread()))$

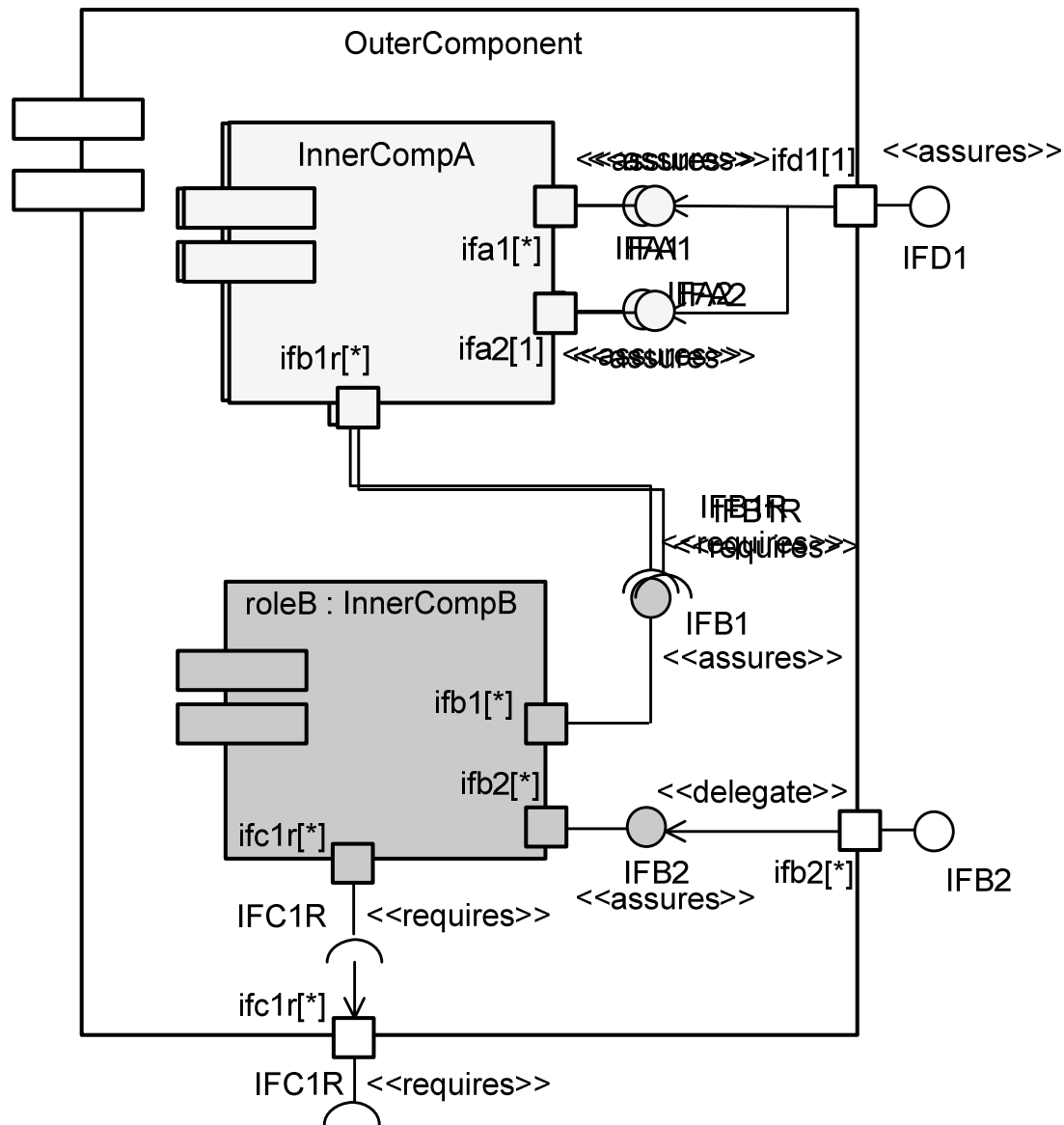
$evaluation_s^{e+1} = evaluation_s^e \triangleleft \pi_6(behaviour(snapshot_s^e, next_thread()))$

$execution_s^{e+1} = execution_s^e \triangleleft$

$(\pi_7(behaviour(snapshot_s^e, next_thread())) \triangleleft \pi_7(message_execution(snapshot_s^e)))$



- State of the DisCComp specification art
 - Remember: current state of specification technique does not reflect the state of system model (synchronous method calls)
 - UML 1.x -> UML 2.1
 - specification of pre-/post-conditions causes massive overhead
- Main idea:
 - Static description: UML component and class diagrams
 - Abstract behaviour description of required interfaces by using OCL invariants, pre- and post-conditions
 - Textual (imperative) behaviour specification of assured interfaces
 - Generation of pre- and post-conditions for assured interfaces by analyzing imperative specifications, when wiring components



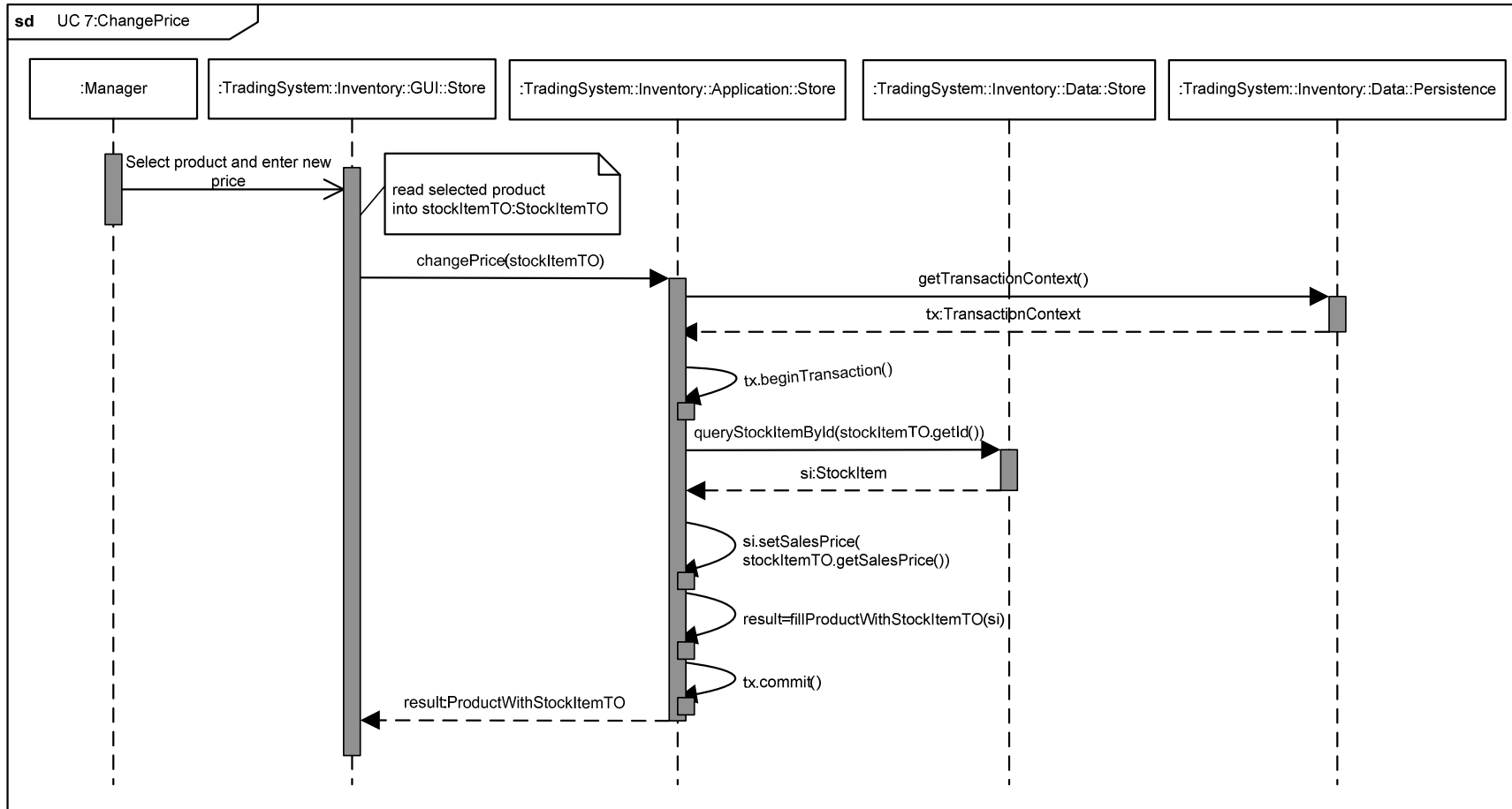
- Focus here: extend existing fine-grained language by introducing some keywords with defined semantics.
- For example, creating instances:
 - `ifInst : IfType = NEW INTERFACE IfType [CONNECT BY ConnType]`

Create new interface instance of type `IfType`. Assign it to the “current component”. Connect it with current interface (optional).
 - `connInst : ConnType = NEW CONNECTION ConnType TO ifInst`

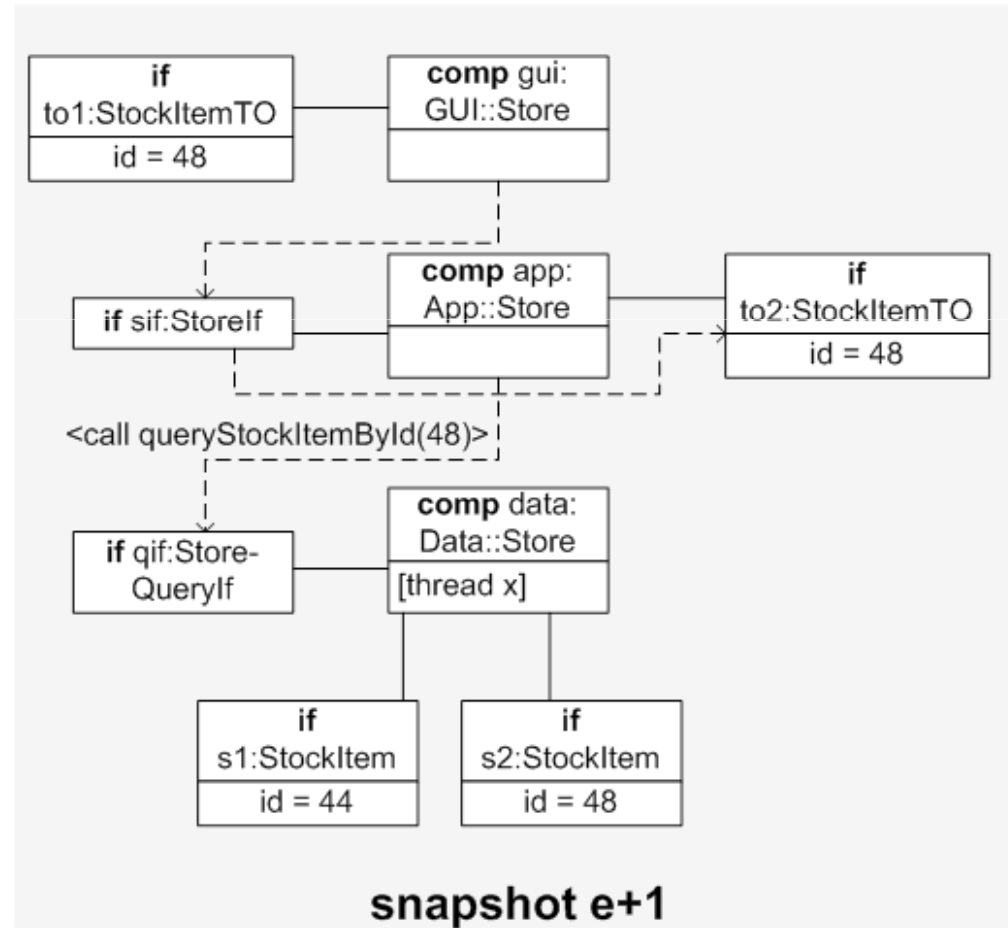
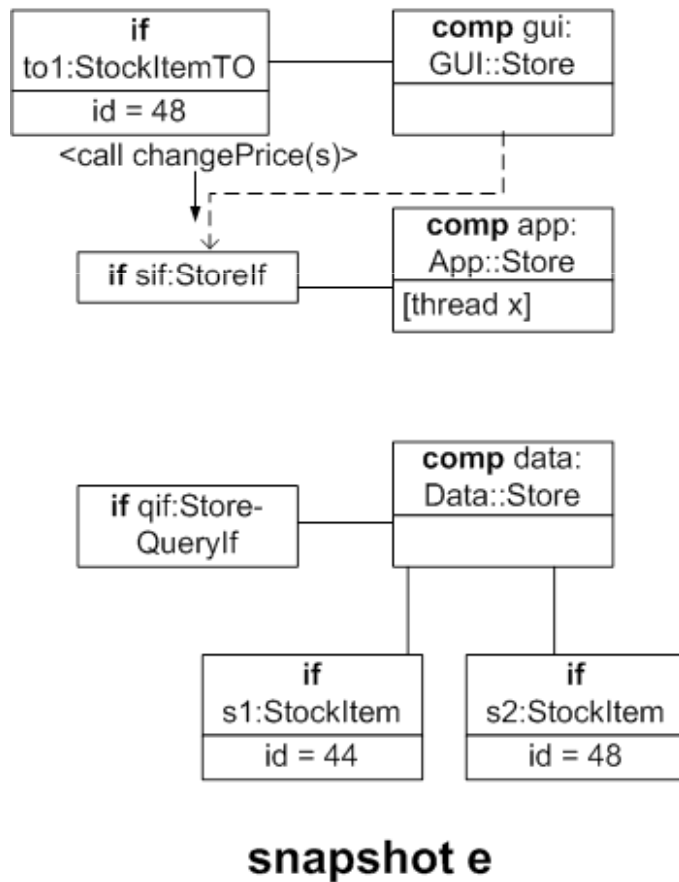
Create new connection between the current interface and `ifInst`. Types must be consistent to the component and class diagrams.
- Return values:
 - **CONNECT ifInst TO CALLER AND REASSIGN**

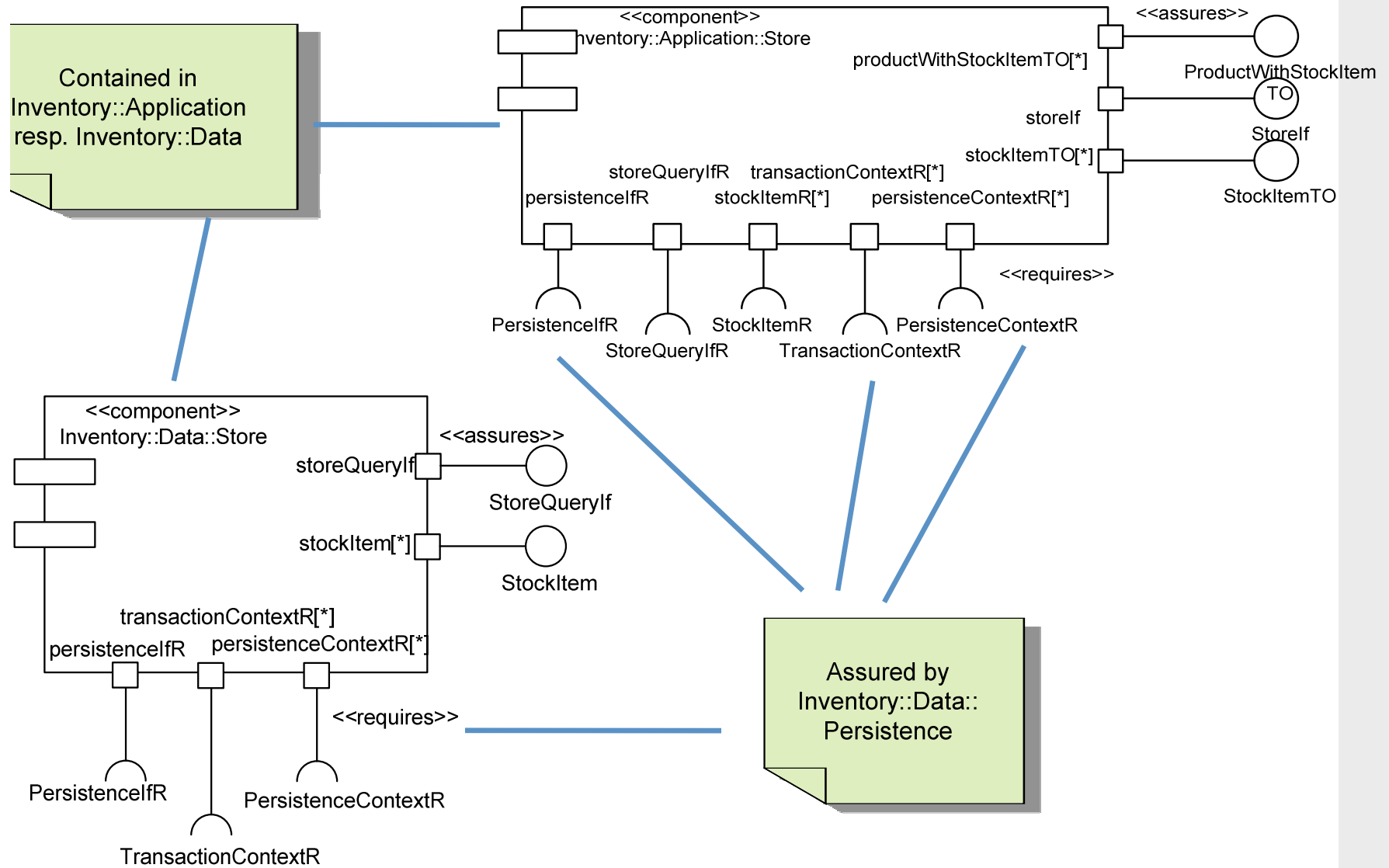
Leave method, return to calling interface, and leave `ifInst` to the calling component.

The Modelled CoCoME Cutout



- Simplified behaviour (without technical components)






```
INTERFACE StoreQueryIfR
```

```
  METHOD queryStockItemById(long sId): StockItemR
```

```
  Pre: sId >= 0
```

```
  Post: let queriedItems : Set(StockItemR) = stockItemR->select(s | s.getId()=sId) in
```

```
    if queriedItems->notEmpty then
```

```
      result = queriedItems->first();
```

```
    else
```

```
      result = NULL
```

```
    endif
```

```
  END METHOD
```

```
END INTERFACE
```

```
INTERFACE StockItemR
```

```
  METHOD getId():long
```

```
  Post: result = self@pre.getId()
```

```
  END METHOD
```

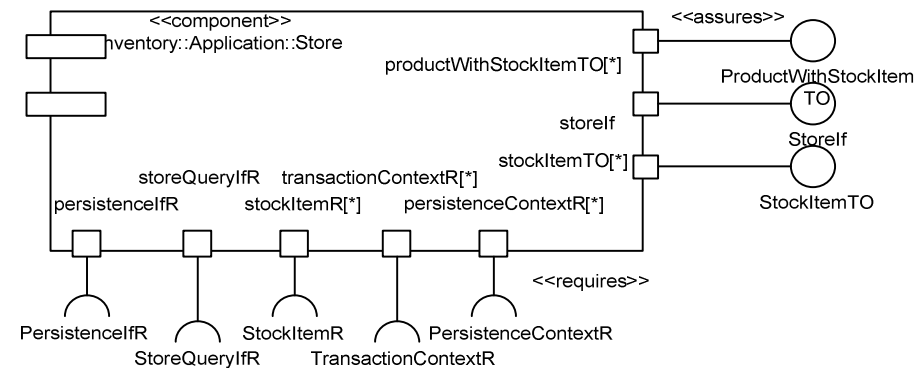
```
  ...
```

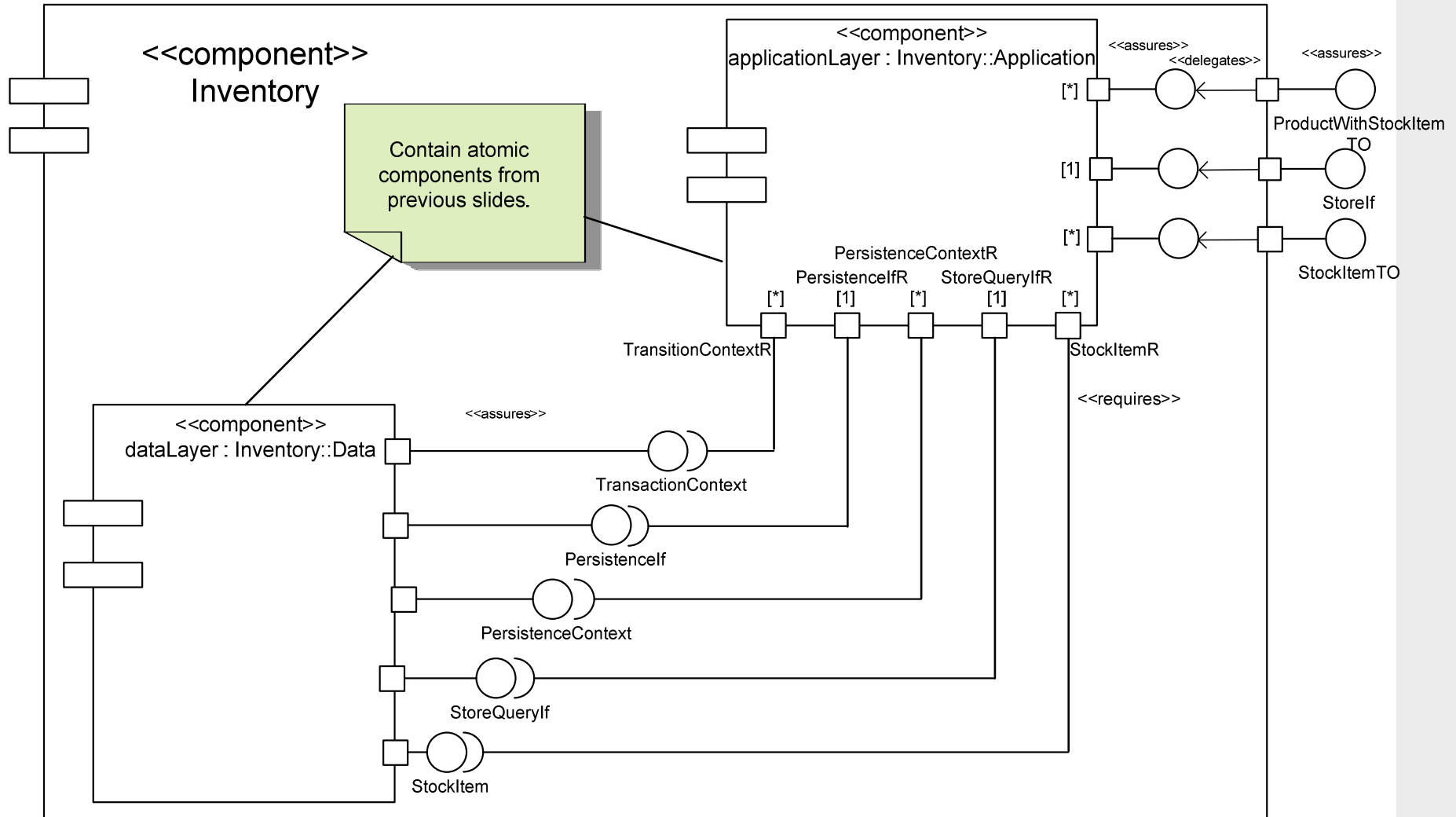
```
END INTERFACE
```

INTERFACE StoreIf

```

METHOD changePrice(StockItemTO stockItemTO) : ProductWithStockItemTO
    result : ProductWithStockItemTO := NEW INTERFACE ProductWithStockItemTO;
    pctx : PersistenceContextR := persistenceIfR.getPersistenceContext();
    tx : TransactionContextR := pif.getTransactionContext();
    tx.beginTransaction();
    si : StockItemR := storeQueryIfR.queryStockItemById(stockItemTO.getId());
    IF (si != NULL) THEN
        .
        .           //copy data to result object if si != NULL
        .
        .           CONNECT result TO CALLER AND REASSIGN;
    ENDIF
    RETURN NULL;
END METHOD
    
```





Conclusion

- DisCComp provides a formal model for distributed concurrent components
 - Supports asynchronous and synchronous communication (as required in CoCoME)
 - Specification technique partly based on UML and OCL, modular specifications by contracts
- Lessons learned
 - Adequate specification technique: we modelled the cutout rather quickly (compared to early DisCComp specifications)
 - We were able to model the functionality of the cutout in terms of DisCComp
 - OCL is troublesome

- Limitations
 - Non-functional properties are not considered
- Future work
 - Semantic foundation of specification technique has to be completed
 - Generation of pre- and post-conditions: what is possible?
 - Extend tool support
 - Specification tool *DesignIt* has to be modified according to new specification technique
 - Extension for generation as mentioned above

Thank you for your attention!

Any Questions

