ECONET Project
PRAGUE 2007 - WORKSHOP REPORT

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2007, 3-7 september

supported by

ÉGIDE

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Executive Summary

An Egide-sponsored workshop was held at the Department of Software Engineering of Charles University in Prague. This workshop was the first one of the ECONET Project Nr 16293RG entitled, "Behaviour Abstraction from Code, Filling the Gap between Component Specification and Implementation".

The workshop was convened in response to the objectives of the first year of the project plan which are remind here

- Determination the field of application (boundaries of Java concepts and idioms).

- Settings of the major principles to abstract behaviours for software components (into Kmelia, SOFA and STS) from Java code.

- Experimentations on existing code.

- Studying and proposing a pattern for annotating EJB components in order to better support RE (behavior abstraction from code).

- Integration of the verification of guards using OCL (and OCLE).

- Documentation, research report and workshop preparation.

More precisely, the aims of the workshop were (1) to improve the knowledge of the participants on each other activity and background, and (2) to take concerted decisions on the project issues (concrete objectives, tasks, organisation, responsabilities, deliveries, planning...).

On these points the workshop was a satisfactory step in the project, thanks to the participant motivation. The following issues have been discussed: detailed competences of each participant on the project topics, comparison of component models and development approaches, concrete objectives of the project, selection of the kind of code to be abstracted, shared techniques and tools, common benchmark, etc. The working sessions enabled (1) to establish a common and shared concrete vision of the project, (2) to delimit the project objectives and context (nature of the Java code, benchmark, subset of the abstract models), (3) to divide the project into parts, which are easier to handle, (4) the definition and repartition of individual tasks.

A project architecture was drawn after fruitful exchanges accompanied with the definition of tasks, with balanced responsabilities and partnerships. This projects includes three distinct but complementary parts:

- Structural Abstraction from Java Code.

- Behavioural Abstraction from Java Code.

- Metamodel Definition and Consistency Verification.

Each part (subproject) constitute a challenge since there are ongoing research on it. Interfaces between the parts have been roughly defined. A common benchmark has been proposed to avoid support mismatch.

A key recommendation from the workshop is that the participants should tackle the objectives in a limited (but extensible) context in order the produce results in a 2-year project. Nevertheless a contribution on the fields is expected. A secondary recommendation from the workshop is to pursue a cross fertilisation on the abstract component models and metamodels.

The workshop concluded by some guidelines to the next workshop that should hold in Nantes in the beginning of 2008.

This report relates what happened in the workshop.

Contents

Chapter 1

Introduction

In this part we remind the context of the workshop, its preparation, organization and the program.

1.1 The `16293RG` ECONET Project

The activity described in this report is supported by Egide in the context of ECONET Projects[1]. This section gathers the main features of the `16293RG` ECONET project.

- Title: Behaviour Abstraction from Code

- Subtitle: Filling the Gap between Component Specification and Implementation

- Type: Research and Technology Development Project

- Duration: 2 years

- Domain: Sciences and Information Technology

- Partners: COLOSS (French) - DSRG (Czech) - LCI (Romanian) - OBASCO (French)

1.1.1 Motivations

The project takes place in a specific domain of Information Technology, called Component Based Software Engineering whose goal is to provide languages, methods, techniques and tools for software developpers. The field of component-based software engineering (CBSE) became increasingly important in software construction approaches because it promotes the (re)use of components, also called Components Off The Shelf (COTS), coming from third party developers to build new large systems. The components are scalable software modules (bigger units than objects in object-oriented programming) that can be used at the high levels of abstraction (software architectures, design) and the low levels (programs, frameworks).

The goal of the project is to contribute to the reverse engineering way by developing techniques for extraction of abstractions from code (including some component interface description) and for verification of the abstractions against the code, *e.g.* to check an in-line bank service with no available code, to check that a client component is compatible with an implemented component.

The core project is to establish a link between component code and component specifications. The advantages of abstraction are to check the conformance of component code and component specifications, to statically check various properties of the components such as safety and liveness. To be pragmatic we have to restrict this huge mapping according to the partner's experience.

1. The source model (implementation level) is limited to Java code. As regarding the problem of obtaining an abstract specification of a component using as input the component code, this problem cannot be solved in a satisfactory manner if the code does not contains appropriate comments, inserted in a well defined pattern, or if the code is not limited to a consistent subset of concepts.

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[1] http://www.egide.asso.fr/fr/programmes/econet/

2. The target model(s) (specification level) are abstract component models inspired from the ones of the partners. Instead of studying only the structural features of the system, we plan to work on *behavioural* abstraction from Java code. Behaviour [PV02, AAA06, PNPR05] is related to the dynamic and functional features of the components and services. In particular, dynamic behaviours describe the dynamic evolution of components, connectors or services. The mechanisms used for component specifications are grounded on different formalisms: design by contract (realized by using assertions), algebraic specifications, statemachines, regular expressions and so on. Each above mentioned formalism offers a set of advantages and has some drawbacks. Design by contract, a declarative specification, supports just an "incomplete" behavior specification. Algebraic specification is, in most cases, difficult to understand by people working in the industry and not all kind of components can be specified. The statemachines and regular expressions formalisms are suited for dynamic descriptions.

1.1.2   Partners

The partners are four research teams which have competences on the project topics.

- **COLOSS**: COmposants et LOgiciels SûrS
  Safe Component and Software ⤳ Component System Verification
  http://www.lina.sciences.univ-nantes.fr/coloss/

- **DSRG**: Distributed Systems Research Group
  SOFA model ⤳ previous work = basis for the project
  http://dsrg.mff.cuni.cz/

- **LCI**: Laboratorul de Cercetare in Informatica
  Computer Science Research Laboratory ⤳ OCL, MDD, Tools
  http://lci.cs.ubbcluj.ro/

- **OBASCO**: OBjects, ASpects and COmponents
  Previous work on Java and Components
  http://www.emn.fr/x-info/obasco/

The four teams have complementary knowledge and background on the project domain. The goal is therefore to compare and exchange the point of view, and to integrate the new ideas and techniques in the current proposal.

1.1.3   Initial Plan

The project is established for two years. The initial planning was organised as follow:
First year:

- Determination the field of application (boundaries of Java concepts and idioms).

- Settings of the major principles to abstract behaviours for software components (into Kmelia, SOFA and STS) from Java code.

- Experimentations on existing code.

- Studying and proposing a pattern for annotating EJB components in order to better support RE (behavior abstraction from code).

- Integration of the verification of guards using OCL (and OCLE).

- Documentation, research report and workshop preparation.

Second year:

- Refinement and classification of the principle and techniques.

- Study of the verification of assertions with OCL.

- Reverse engineering from EJB code to EJB specification realized in JML or OCL.

- Experimentation with larger case studies.

- Documentation, research report and workshop preparation.

Once the context has been introduced, we present now the workshop itself.

1.2 The Workshop at Charles University of Prague

The workshop is a major milestone for the first year of the project. This section presents the project evolution until the workshop helds.

1.2.1 Preparation

Since the project has been accepted by Egide in march 2007, the exchanges between the partners became more frequent and precise at this date. Some of the exchanges are stored in the Project Wiki.

This wiki was installed at LINA (University of Nantes) in april 2007. It includes discussions, a repository for project and workshop material, etc.

Figure 1.1: Project Wiki

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:start
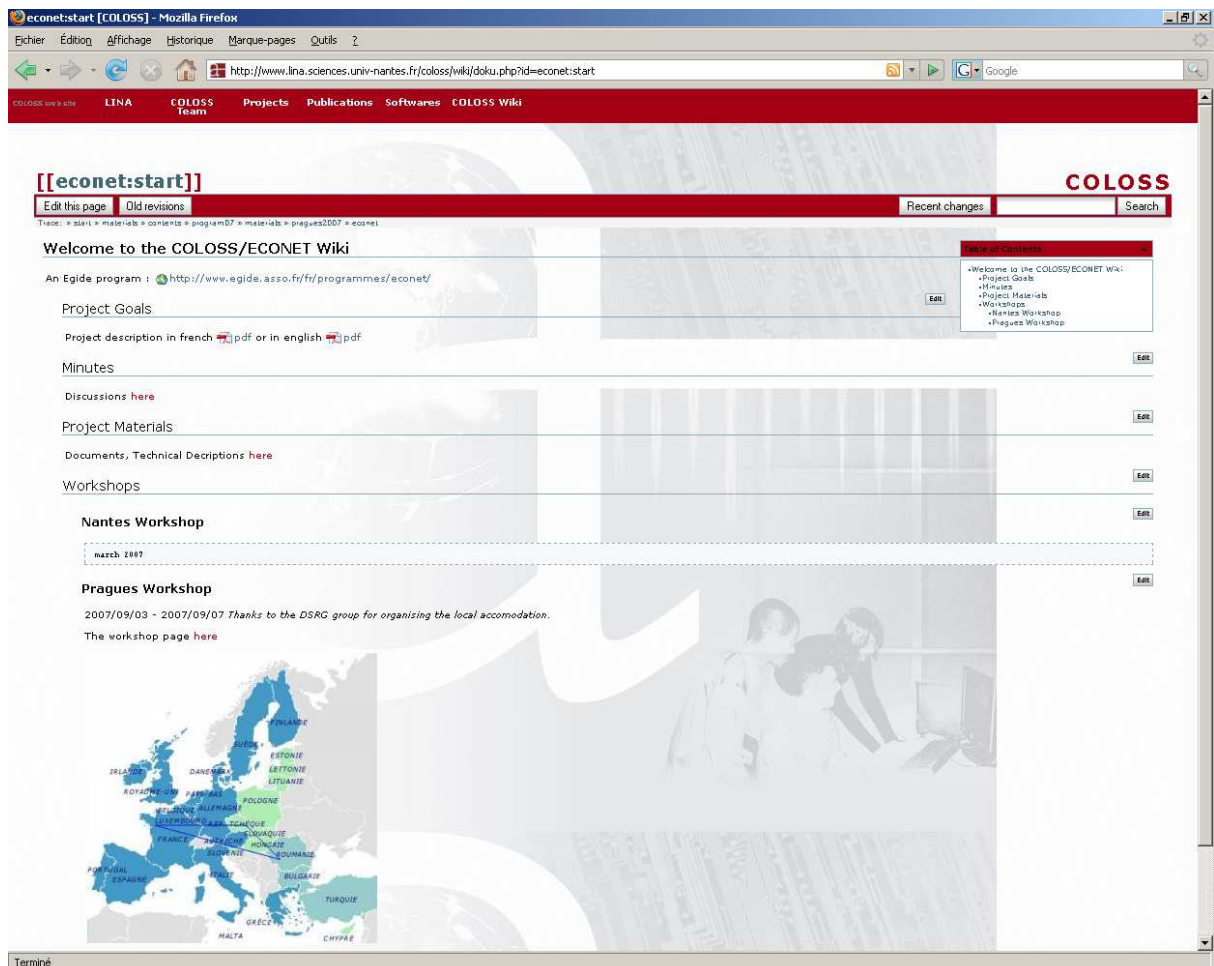
The workshop was initially planned on the end of june, possibly joinly to the TOOLS conference. Actually this was not possible and it has been delayed to the first week of september.

A special group of pages have been writtent for the Workshop (figure 1.2). The URL address is:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007
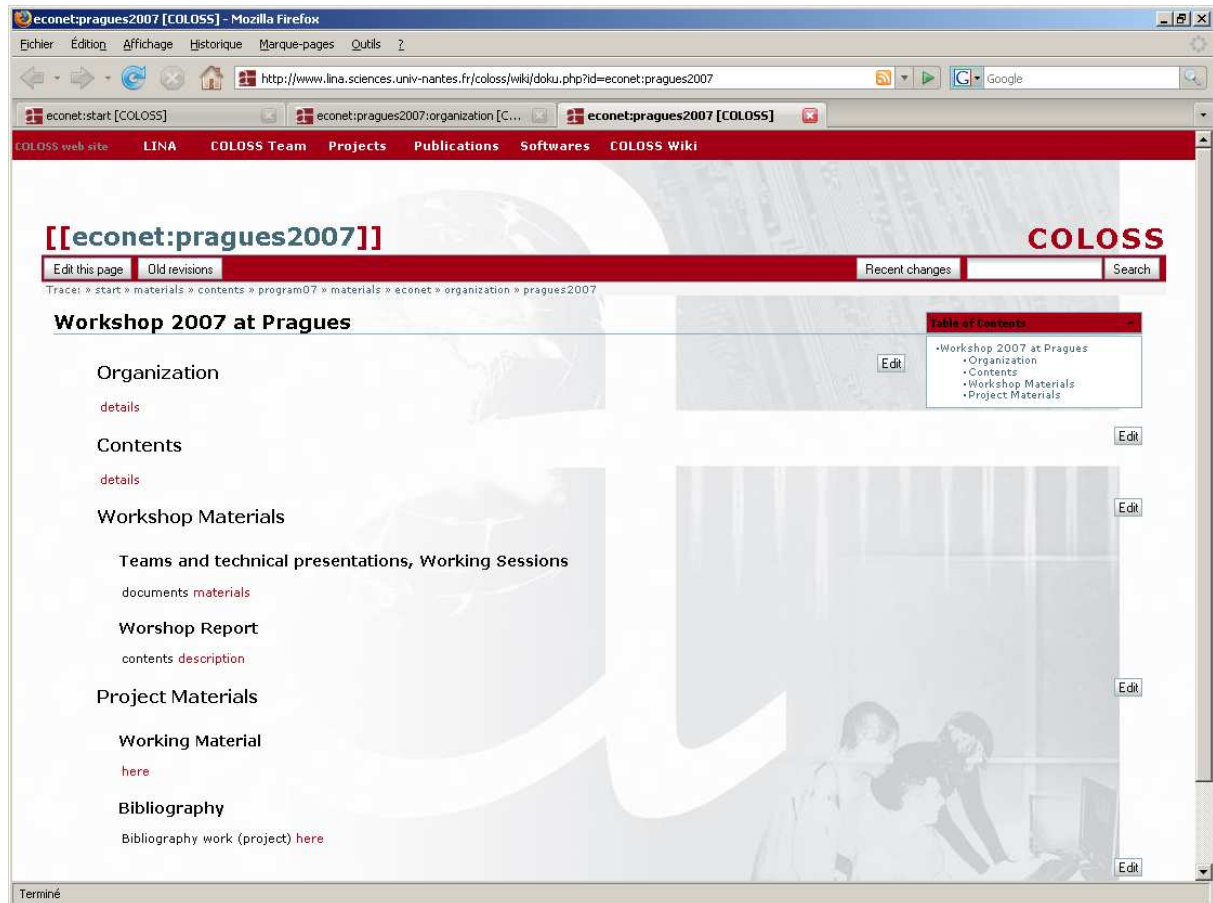
Figure 1.2: Workshop on the Wiki

1.2.2 Organisation

The workshop organisation was handled by Pascal André and Ondrej Sery. The local organization committee included Ondrej Sery, Frantisek Plasil, Petr Hnetynka and Jan Kofron.

Detailed informations are given on the wiki site (figure 1.3).

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:organization

1.2.3 Objectives

The following Workshop Objectives and Delivery were a first throw and kept many open issues.

- Objectives ⤳ State of The Art + Clear Application Context

    – build a reference bibliography of the reverse engineering domain
      concepts, related work and comparison, mains issues, approaches, plateforms and tools (JPF, Bandera,...)

    – set the source area
      subset of Java concepts, libraries, components, examples

    – set the target area(s)
      SOFA, Kmelia, Vercors, ... - contracts, protocols, inheritance...
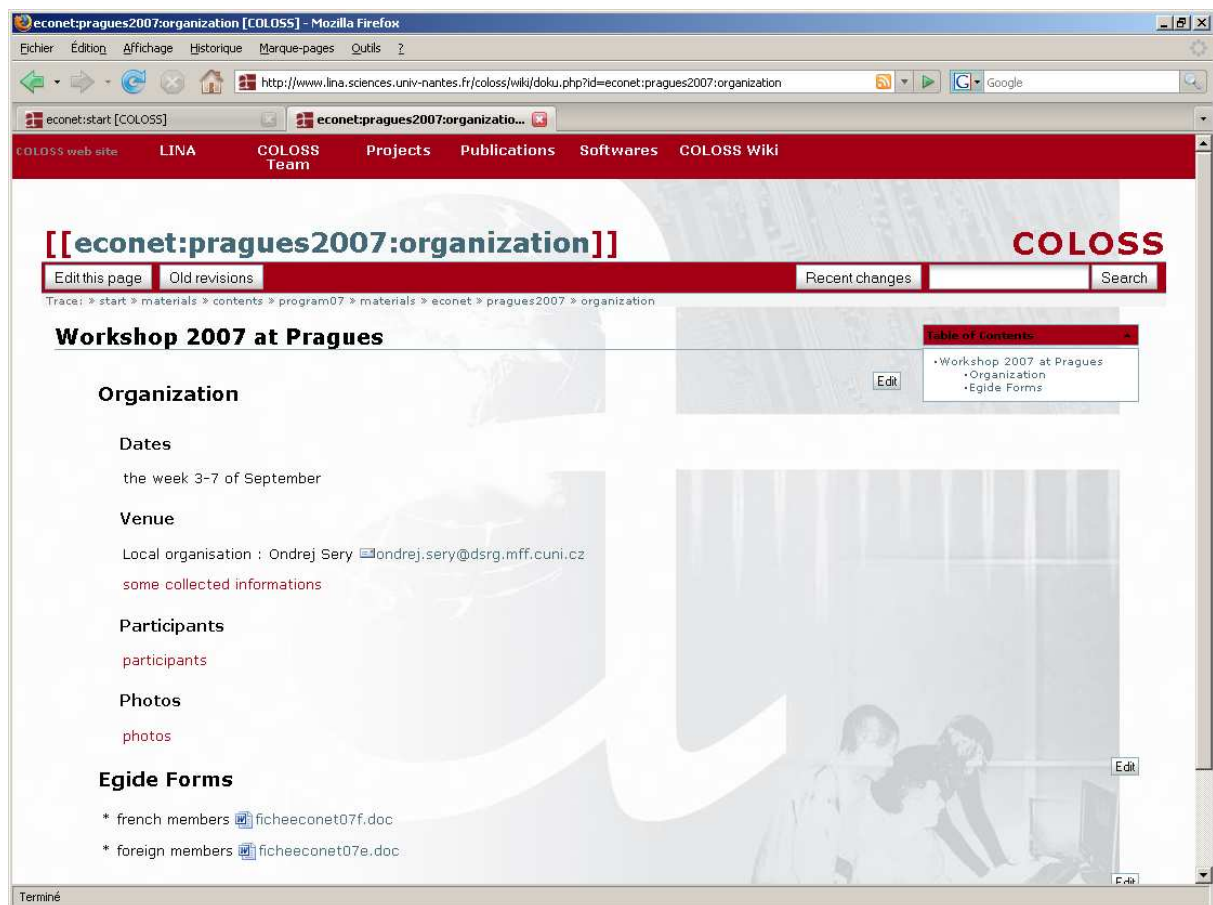
Figure 1.3: Workshop Organization on the Wiki

   – initiate some directions to follow in order to process the reverse transformation
patterns, rule based system, combination of several existing tools

- Delivery
  A report for the project first year evaluation + plan the second year with individual objectives

1.2.4 Participants

The detailed list is arranged according the the alphabetical order of first names.

- *Dan CHIOREAN* - LCI
- *Dragos PETRASCU* - LCI
- *František PLÁŠIL* - DSRG
- *Gilles ARDOUREL* - COLOSS
- *Jacques NOYE* - OBASCO (absent)
- *Jan KOFROŇ* - DSRG
- *Jean-Claude ROYER* - OBASCO

- *Jiří ADÁMEK* - DSRG
- *Ondřej ŠERÝ* - DSRG
- *Pascal ANDRE* - COLOSS
- *Pavel PARÝZEK* - DSRG
- *Petr HNĚTYNKA* - DSRG
- *Tomáš POCH* - DSRG
- *Vladiela PETRASCU* - LCI

1.2.5 Program and Schedule

We present here an overview of the workshop program. It was organised in two parts

- Day 1 and 2 are dedicated to workshop presentations. The durations and schedules leave time for numerous discussions...

    - Presentation of the teams (recent work, projects, tools, ...)
    - Technical presentations

- Day 3, 4 and 5 are dedicated to the project work (context, goal, process, tools, practical organisation and responsabilities)

- Social events

More details are given on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

1.3 Report Contents

In the remaining of the report, we provide more informations on the presentation sessions (section 2.1 of chapter 2) and the working sessions (section 2.2 of chapter 2). In chapter 3 we present the project architecture which is a main result of the workshop.

Warning

This report has been mainly written by Pascal from his personnal notes. There may remain english errors, misunderstanding, transcription errors, and so on. He apologise for these errors.

Chapter 2

Workshop Sessions

This chapter collects the main events and informations of the workshop sessions. We first begin by the presentation sessions where the participants present themselves, their team and technical contributions (section 2.1). Then we summarise in section 2.2 the contributions of the working sessions where the participants discussed on the project (issues, structure, tasks, technical aspects, tools...).

The detailed program is given on the wiki at:

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:
pragues2007:program07

The slides, pictures and discussions are stored on the wiki at:

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:
pragues2007:materials

2.1 Team and Technical Presentation Sessions

2.1.1 Introduction

During the development process of an information system several models are produced (manually or automatically). A main objective of the ECONET partners are to contribute to improve both the models and the production. In particular, all partners are concerned with the verification of models and programs.

To simplify, let consider at least two levels for the models produced during the development process: an abstract model and a concrete model written in a programming language *i.e.* in Java. From the verification point of view, the goal is to assert properties (by proof or by model checking) on both the abstract and the concrete models. One way (the formal method approach) is to assert properties on the abstract model, then to refine it into a concrete model and prove the refinement. Another way is to assert properties on both levels.

In this workshop we are mainly interested in the model-checking approach for property verification. Model checking is the process of checking whether a given model satisfies a given logical formula. The concept is general and applies to all kinds of logics and their models. A simple model-checking problem is testing whether a given formula in the propositional logic is satisfied by a given model. In case of properties to be checked, the most common ways to express them are via a temporal logic (LTL, CTL) and in the form of assertions. However, it is also possible to check for a predefined set of properties - deadlocks or properties specific to a certain class of systems such as device drivers.

One can admit that model checking at the abstract level is well-known and there exist techniques and tools for model-checking abstract models but model-checking concrete models is to discover. Model checking of software is a popular research topic nowadays, mainly because there are several issues that have to be solved before the technique can be used for real-life applications.

How can we do ? We can check the code it self with appropriate tool. This is not convenient because the tools are not mature in this domain and the concepts at the concrete level are quite different from those of the abstract level, the informations are numerous and more detailed, model-checking quickly faces the problem of state explosion. Another approach is to filter the informations:

- by selecting informations of the code model checker (as explained in the presentation of Pavel Parisek)

- by abstracting informations of the code , this is main issue of our Econet Project: extract abstraction *i.e.* in order to apply model checking at the abstract level. Two cases are possible whether an abstract model exists or not.

The talked given during the presentation sessions addressed contributions on the above topics. Many of them present an abstract model and model checking techniques (Petr Hnetynka, Jan Kofron, Jean-Claude Royer, Pascal André). The talk of Pavel Parizek is more precisely about the problem of model checking concrete models against abstract ones). The talk of Dan Chiorean was more general because it works on models and metamodels, moreover it assumes a quite different view of model checking, that is the verification of consistency and completion of models.

Note that we summary the main issues of the team presentation and forget among others, history and members details.

2.1.2   Monday, September 3, 2007

Time	Title	Speaker
10:30	Welcome and Program	Pascal André
|	Participant presentation	each participant
|	Local Organisation	Ondrej Sery, Petr Hnetynka
12:00	DSRG Team presentation and position	Frantisek Plasil
13:30	SOFA overview	Petr Hnetynka
|	(Extended) Behavior protocols + demo (from CoCoME contest)	Jan Kofron
17:00	Checking behavior protocols against code using Java PathFinder	Pavel Parizek

DSRG

Frantisek Plasil presented the Distributed Systems Research Group, one of the research groups of the Department of Software Engineering of Charles University. Its areas of interest are:

- Software Performance (Regression benchmarking),

- Software Components (SOFA, Fractal)

    - Architecture/Component models,

    - Design (Use cases, Behavior specification of components - Model checking),

    - Deployment (Connectors - Addressing environment heterogeneity)

DSRG focuses on research in distributed systems, particularly the construction of component and service middleware and its software engineering aspects - system architecture, formal definition and verification of behavioral properties, performance evaluation. This focus is reflected in the long running SOFA project, which deals with a distributed component model that provides advanced features such as formal verification of component properties, and which is provided as an open source platform. Other major projects that DSRG recently participated in include the ITEA PEPITA & OSMOSE & OSIRIS projects and industrial projects with partners such as France Telecom, Borland and Iona.

The areas of contribution to ECONET are in general a cross fertilization and joint publications. More specifically, they are providing SOFA, enhancing EBP by additional features from STS/eLTS (to enhance expressive power of EBP), extracting protocols (EBP) from code (at least BP from code), defining well-formed rules for SOFA components, prviding a benchmark (CoCoME example).

SOFA overview

As chief architect, Petr Hnetynka presented an overview of SOFA 2. The SOFA 2.0 is the new version of the SOFA component system. It focus on removing limitations of the current SOFA implementation (inconsistencies between implementation and specifications e.g., protocols vs. connectors, architectures vs. dynamic reconfiguration,...), clear design and properly balanced support of advanced features. These are mainly:

- model-driven design

- hierarchical architectures

- support for dynamic architectures

- support for multiple communication styles

- formal modeling of both functional and non-functional parts of components

- transparent distribution

- behavior validation

It is implemented in Java (as SOFA was). The implementation freely available (LGPL license) http://sofa.objectweb.org/
The presentation included detailed informations on

- Component model

  - meta-model

  - dynamic reconfiguration (dynamic architectures)

  - connectors

  - control parts (non-functional)

  - versioning

  - behavior specification

- Implementation

  - component lifecycle

  - runtime environment

  - usage, tools, current status

Questions

About consistency (Metamodel well-formedness rule) the project organization in two parts on the group (components archi / behavior specifications) lead to some independent (and not compatible) evolutions. Well-formedness rule are informal except for protocols.

The micro-components have a similar concept in fractal (monitoring methods and repository).

The 'aspects' are not really those of Aspect Oriented Programming.

About the link between specification and code, non-SOFA components can be encapsulated in wrappers, connectors can be RMI, Corba, the implementation is java specific with SOFA knowledge and annotations (future).

Communication style in connectors can be any communication style (method invocation, shared memory, ...).

Modeling and Verifying Behavior of Software Components in SOFA 2

Jan Kofron presented the (original) Behavior Protocols of SOFA and the extensions of Behavior Protocols (EBP) of SOFA 2.0. Each part was organised as follow (presentation, description, verification, experience, demo).

The problem of behavior verification is undecidable in general. There are two ways to face it: (1) To use behavior description languages which describe behavior of the software precisely and to put up with the fact that the tools will never stop for some inputs (behavior descriptions). (2) To use behavior description languages, which are not expressive enough to describe behavior of software precisely, but the verification of the specifications is decidable. We have chosen the second approach. Therefore, a behavior protocol should be seen rather as an approximation of a component's behavior. The most important benefit of this approach is the existence of a fully automatic behavior verification procedure (implemented in our behavior protocol checker).

The purpose of behavior protocols is to specify the behavior of software components, so that interesting properties of their behavior can be verified (not only the operation signatures) using model checking techniques for example. Model checking is one of the approaches to formal verification of finite state hardware and software systems. A model checker usually accepts a finite model of a target system and a property expressed in some property specification language, and checks whether the model satisfies the property via traversal of the state space that is generated from the model.

Behaviors are an abstraction of the component interactions and can be represented by LTS, use cases, sequence diagrams, process algebra expressions, etc. In SOFA, behaviors are described by processes (regular expression and communication primitives as in process algebra). Checking protocol compliance (both horizontally and vertically) is similar to checking component behavior compatibility. Compliance means: absence of communication errors(Bad activity, No activity, Infinite activity) which can be found automatically) and verified separately for particular levels of nesting (hierarchy).

In SOFA two checkers were implemented: the Behavior Protocol Checker (BPC) (proprietary explicit state model checker for BP, written in Java, uses Parse Tree automata for state space generation, able to verify state spaces of the order 10^7 states, May run several days) and the dChecker (again proprietary tool, distributed state space traversal, significantly faster than BPC, state spaces of the order 10^7 for each computer i.e., entire state space of the order of 10^8 state).

Several flaws of BP were identified during the specification: lack of synchronization mechanisms (impossible to synchronize more than two components), lack of expressiveness (absence of macros caused parts repetition in the specification which make it hard to fix the errors, absence of variables caused overspecification, absence of a way to express common patterns, e.g. until loops, caused unreadable specification).

This led to extensions of BP on data (method parameters and local variablescan be of enumerated types), synchronization (special events: joining events for synchronization of more than two components) and Until loops (just a syntactic abbreviation to enhance the readability). Performance issues were also a motivation, the new solution is a transformation of EBP into Promela, the input language of the Spin Model Checker. The performances were improved on the same benchmak (CoCoME).

Questions

Guards are possible, they are branches on local state.

A visual representation of the protocols is possible via parse trees. The component picture is manually done with Visio in a UML flavor.

Local checks are performed. Promela checkes both horizontal and vertical compliance. One to many is in the metamodel.

About feedfack, there are solutions...

Checking behavior protocols against code using Java PathFinder

Pavel Paryzek addresses a topic that is close to the project concerns[1]: the verification of conformance between behavior specifications and code. This work locates in the general problem of model-checking programs and especially Java code. Remind that in the previous presentation, Jan Kofron talked about (abstract) model checking. The problem addressed here is to do it at the implementation level.

A general problem of model checking is the necessity to create a model of the system to be checked. Manual construction of the model is an error-prone process, and even if the model is automatically extracted from a specification of the system or from the source code, it is an abstraction - therefore, a model checker may find errors in the model that are not present in the original program and vice versa. A solution is to use a model checker that does not need to have a model, but works directly with the implementation of a target system. There are several difficulties encountered with such an approach: code model checkers are need, the models and properties handled are low-level, there is a problem of state explosion since an abstract state collapse many concrete states, partial models are to check and not only full programs. The talk of Pavel deals with these difficulties.

As to software model checking at the program source code level, a crucial problem is the size of state space triggered by the model of a program (i.e. the problem of state explosion). Despite that, there exist such model checkers. For Java programs, these are most notably the Java PathFinder (JPF) and Bandera tools. (An advantage of JPF over Bandera is that the most recent release of the latter is an alpha version, not being fully stable yet, and that JPF is also more extensible). The properties checked are either predefined (e.g. absence of a deadlock) or to be specified in LTL (Bandera) and via assertions related to the code (JPF). A typical feature of both Bandera and JPF is the combination of static program analysis and model checking. The former is used to create a program model; to lower the state space size, abstraction techniques are applied - these include partial order reduction and data abstraction. State explosion can be also mitigated by the decomposition of a software system into small and well-defined units, components. Typically, a software component generates smaller state space than the whole system

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[1]See the remarks in the introduction section

and therefore can be checked with fewer requirements on space and time. Nevertheless, model checking of code of software components usually brings along the problem of missing environment, what means that it is not possible to model check an isolated component, because it does not form a complete program with an explicit starting point (e.g. the main method). In order to solve this obstacle, it is necessary to create a model of the environment of the component subject to model checking, including the specification of possible values of method parameters, and then check the whole program, composed of the environment and component. A specific feature of software components is the existence of ADLs (Architecture Description Languages) used to specify component interfaces, and first of all composition of components via bindings of their interfaces (i.e to specify the architecture of a component-based application at a higher level of abstraction than code). Some ADLs even include the option to specify behavior of the components, typically in a LTS-based formalism. An obvious challenge, not addressed yet to our knowledge, is to check the code of software components against a high-level behavior specification provided at the ADL component specification level.

In this work, it is assumed that each primitive component (Java code) is compliant with its frame protocol (we thus have an abstract model). It has to accept/issue exactly those method call related event sequences on its frame that are specified by the frame protocol. The goal is to design an algorithm and a tool for checking compliance between Java implementation of a primitive component and its frame protocol via Java source code or byte code analysis.

The experimentation is done with JPF (Java PathFinder) a model checker for Java programs which is highly customizable and extensible ; it is able to check only low-level properties (such as deadlocks, uncaught exceptions) with non-deterministic value choice. It supports a `Publisher/Listener` pattern that can can watch the course of the state space traversal and check for specific properties. Checking by JPF is not directly possible because JPF is able to check only low-level properties. So an extension of JPF is necessary to check the compliance between Java code and frame protocol is a high-level property: *a cooperation between JPF and the BP checker is defined with a backtracking policy*.

More over JPF accepts only complete Java programs: *an automated generation of component environment is proposed to solve the problem of missing environments* (for components). The model should force the environment to call a certain method of a particular provided interface at the moment the component expects it and to accept a certain method call issued on a particular required interface at the moment the component "wishes" to do so. Two options are possible:

- Inverted frame protocol which is constructed from the frame protocol by replacing all the accept events with emit events and vice versa. This option models the most general valid environment

- Context protocol which specifies actual use of the target component by the other components in the particular hierarchical architecture. This option models the simplest valid environment.

Context protocol is more suitable because the component application typically exploits a subset of functionality provided by the target component and completeness and feasibility are taken into account at the same time. But context protocol is time consuming and there are no java construct for acceptance of a method call calling protocol.

Last, two techniques are proposed to reduction of environment's complexity (state explosion problem) : reduction of level of parallelism (static code analysis, concurrent metrics) and reduction of repetitions.

Questions

You used the base BP model checking for this experimentation, did you investigate it in the context of EBP with promela and SPIN. No really we shoukd have the same problems probably.

Do you think JPF can be used for reverse engineering ? Not exactly JPF provides executions abstractions through the traversal of execution flows on ibjects and classes. It can be associate to other tools via its listeners.

2.1.3   Tuesday, September 4, 2007

Time	Title	Speaker
09:00	Introduction	Pascal André
		LCI Team presentation and position	Dan Chiorean
12:00	(UML) Model Checking - an OCL Based Approach	Dan Chiorean
13:30	OBASCO Team presentation and position	Jean-Claude Royer
	Introduction Illustrating The STSLIB Project:	Jean-Claude Royer
	Towards a Formal Model Component Based on STS	
	COLOSS Team presentation and position	Gilles Ardourel
18:00	The Kmelia Component Model Hierarchical Service Description and Analysis	Pascal André

Welcome

Pascal André introduced the program of this second day of presentations including three team presentations and technical talks.

LCI

Dan Chiorean presented the Computer Science Research Laboratory (LCI or Laboratorul de Cercetare Stiintifica In Informatica). The Computer Science Research Laboratory was established in 1990, as an auxiliary department of the Mathematics and Computer Science Faculty. The projects handled by the laboratory are included in contracts with the Ministry for Research and Technology and certain academic institutes. Its main purpose being the promotion of research activity in a top field, that of object-oriented technology.

The main products designed and implemented in the lab so far were the mFOOPS environment, and the ROCASE and OCLE tools. The UBB-LCI team participated in the EU Research Project IST 1999-20017 NEP-TUNE. The LCI team obtained recognised results in the areas of model checking. Continuing the work begun in the NEPTUNE IST project, and using some of the results obtained in that framework, the LCI team designed and implemented OCLE, one of the most complete OCL tools existing today. The LCI group is very keen on continuing its work in the above-mentioned domains, with special interest in MOF based repositories, OCL support for all abstraction levels, OCL extensions for transformations and action support, improved code generation, application of MDD (Model Driven Development).

In the context of this ECONET project, the UBB-LCI team brings its experience in the definition of contract and data features in the definition of the component specification and in the verification of consistency between these features and the Java code.

(UML) Model Checking - an OCL Based Approach

Dan Chiorean exposed his point of view on model checking UML models. This research area is taken in the context of latest modeling approaches. This means that it take place in the Model Driven Approach and Languages (MDA, MDE, LDD, DSL) which are characterized by: the use of metamodeling, modeling languages more specialized compared with UML, modeling languages are MOF based languages. In this context, a robust and thoroughly tested MOF model would be in the benefit of all modeling languages Specifying a precise syntax and semantics requires usage of rules (another formalism - algebraic specs) An appropriate OCL support is needed (U)ML uses different formalisms for specifying architectural and behavioral information, therefore checking (U)ML models is quite complex. The results and conclusions obtained in UML Model Checking, can be used, both direct or indirect in checking models specified using the latest modeling approaches.

The objectives are: to ensure that the model conforms with different kinds of rules (UML Well Formedness Rules WFR, Profile Rules PR, Business Model Rules BMR, Methodological Rules MR, Behavioral constraints rules BCR), to use accepted and easy to understand standards in the modelers community to validate the standard specification on real models.

The following results are expected. Realized applications comply with requirements. Application become more reliable. Time needed for developing applications is diminished. The application costs decrease. Promoting reuse to all model element levels (classes, components, patterns, frameworks).

In the context of new modeling approaches, model compilation has to become a mandatory requirement (like source code compilation) In order to accomplish these objectives an incremental and iterative approach is proposed: rule specification, rule validation, identification of reasons for rule failures, model updating - applied to different modeling layers.

Then Dan described some features of UML Models related to OCL and verification rules, and some examples of rules in each category (WFR, PR, BMR, MR, BCR). (UML) Model Checking concerns: Model Completeness, Model Consistency, Model Correctness and Model Architecture Accuracy. Model Checking related activities must be supported by appropriate CASE tools. Some questions are: what could be done in order to improve the state of facts? Is the needed information disposable? Is the textual formalism appropriate?

In previous related work, people mainly faced the (difficult) problem of the model consistency property, but it is only a part of model checking. Moreover as UML is grounded on different formalisms - OCL, STD (ASM), graph theory (Petri nets) - UML model checking requires different approaches. Dan discussed some debatable aspects: multi-view models, lack of formal semantics, management of inconsistencies (inherited cyclic composition inconsistency, dangling type reference and connector specification missing) ...

The last part of the talk is dedicated to tool support.

First Dan sketched some UML/OCL tools (commercial, research) and facts *e.g.* working with medium or large models is difficult. Some main drawbacks restrict a widespread use of tools that support OCL: the lack of coordination between UML services and OCL services, a weak support for interchanging UML models, The proprietary architecture used for repositories The lack of OCL support at M2 level The lack of support for reusing OCL specifications at M2 level The weak support for updating UML models in an interactive manner Compiling UML models before transforming them is not yet an usual activity Unfortunately, only a small part of UML static semantics was tested For some rules, the informal specification is ambiguous Many specialists make general statements about the UML specification drawbacks. They havenŠt tried either to identify the rationale of these drawbacks or to precisely mention the drawbacks Surprisingly, misconceptions and incorrect statements about using OCL and about the language potential can be encountered in many papers ?

Then Dan presented the OCLE tool, developed by LCI, and demonstrate it in live. What distinguishes OCLE from other similar tools is: allows simultaneous access at both M2 and M1 level. implements OCL 2.0 specifications (ensuring compatibility with former OCL versions), extended support for the "def, let" mechanism supports simultaneous multiple views of the same information, supports users in correcting the errors identified by evaluating the rules specified at the M1/M2 level, supports semantic checking of XML documents, supports MDA both by defining and checking profile rules, propose a friendly GUI (Ű the graphical editor ?), supports reuse by means of OCLE projects, supports compiling and evaluating of OCL specifications spread in different files. In order to better accomplish the requirements related to the intuitiveness, rigor, usefulness - the following functionalities were implemented in OCLE: transitive closure operation, neutral printing operations, explicit context specification flexible specification for operations without parameters and the compatibility with the syntax of previous OCL versions Some benefits are the possibility to specify simpler OCL expressions extended possibilities for debugging OCL specifications and support for reusing earlier specifications without any changes.

Checking UML models against Well Formedness Rules using an appropriate UML/OCL tool is the natural way to check UML models and to correct various errors. Many bugs found in the UML static semantic specification v1.5 were fixed and the majority of the rules were tested. Using the OCLE code generator, the Java code implementing the Additional Operations behavior was generated completely and used in the OCLE repository. The OCL 2.0 specification was extended with a set of practical features the solution we proposed for checking XML documents overcomes the drawbacks that all the "classical" solutions carry

Remark

The definition of model checking is quite different from the one in SOFA, KADL, and Kmelia approaches. The main reason is that it works at a model level rather than at the system level.

Questions

A first question was about components in UML and verification tools. A track is the work at Dresden on component instanciation.

Another question was about a formal semantics of OCL 2.0. The answer is the same as UML : see the metamodel.

OCLE (and ROCASE) have been used for both education and industry.

Other questions are summarised in a draft version extracted from PA notes.

- Completeness and consistency of WFR: this is a difficult problem.

- Extension of OCL to some reduced kind of Action Semantics to settle Functional Computation descriptions: it can be done since there exist in fact a family of OCL languages.

- Standard evolutions (what happends when a new version is adopted): the metamodel is download, the crucial point is the MOF dscription.

- Import-export facilities by XMI files and visual representations by DI (Diagram Interchange): tools do not fully comply XMI, and DI is not adopted yet. UML models differ from MOF models. OCLE is not a standalone tool but I have to show and navigate on class diagram, snapshots, use case

- Different problems : non-standard visual notations first tool Rocase (C++), OCLE code generation in MDE, java code generation (model, assertions) parts of the repository have been produced by the tool.

- Java profile for code generation. We use our own policy - run the rules.

- Implementation : libraries for visual, velocity. XMI reader/writer have been implemented

- A big problems is the maintenance: developers moved OCL compiler and evaluator (1st price design contest).

OBASCO

Jean-Claude Royer presented the OBjects, ASpects, and COmponents (OBASCO) team. OBASCO is a joint research group of (the CS Departement of) Ecole des Mines Nantes and INRIA's research unit in Rennes, IRISA (see also OBASCO's home page at IRISA). OBASCO is also a team of Laboratoire Informatique de Nantes Atlantique (LINA, FRE CNRS 2729). The LINA laboratory in "Sciences and technologies of the software" is mainly a cooperation between Ecole des Mines de Nantes and the University of Nantes. It is specialized on two axes : distributed software architectures and computerized decision-making systems.

The OBASCO (Objects, Aspects and Components) research group addresses the general problem of adapting software to its uses by developing tools for building software architectures based on components and aspects. We are (re)using techniques developed in the programming languages domain, and more precisely in object-oriented programming.

The objectives are to solve scalability problems in software engineering and to improve software architectures adaptation. Two main directions are followed the separation of concern (specific programs for specific problems) and correct composition of existing programming artefacts.

The research domain covers topics on the Software Engineering

- Software components and scalability

- Programming languages

- Post object-oriented programming

- Generative programming

    - Sequential, concurrent and distributed
    - Mechanism for separation and composition
    - Objects versus aspects versus components

- Model driven engineering: transformation techniques

according to three swim lanes

- Aspect-oriented programming

    - To explicit links between metaobject and aspect
    - To formalize aspect-oriented models
    - To design and implement a language
    - Reverse engineering of legacy code with aspects

- Software component

  - Explicit protocols for components

  - Property verification for components and architectures

  - Understand relations between aspects and components

- Domain specific language

  - Domain specific language

  - Expressiveness, extensibility and compilation

  - Aspect languages, composition and DSL

OBASCO brings its experience to the ECONET project with an abstract model based on algebraic specification and LTS, and its derivation to Java code (engineering process while in ECONET we work on the reverse-engineering process). A first work was to verify some properties in component systems with data, we have some results here, which make it possible to abstract component protocols with data. We have also shown how to generate Java code from protocol descriptions, such that components can communicate according to their declared behaviour. Finally, we have investigated aspect languages for the modularization of crosscutting concerns defined in terms and through modification of protocols. A question, which we did not study, was the compatibility between some Java code and a protocol. OBASCO is also interested in extending the SOFA approach to introduce data, parameters and guards. Furthermore, we intend to study the integration of these techniques with our on-going effort on the static analysis of properties of protocol-modifying aspects.

Introduction Illustrating The STSLIB Project: Towards a Formal Model Component Based on STS

Jean-Claude Royer presented an approach that covered the whole development models from abstract description to Java implementation.

Component-based software engineering is becoming an important approach for system development. It is assumed here that explicit protocols are integrated into component interfaces to describe their behaviour in a formal way. In this case explicit protocols are often dissociated from component code, it is not ensured that component execution will respect protocols rules. A crucial issue is to fill the gap between high-level models, needed for design and verification, and implementation.

This talk introduces first a component model with explicit protocols based on symbolic transition systems. Some related models are quickly overviewed: Java/A, CADP, Behavior Protocols models (SOFA, PROCOL, CO-OPN), FSP java, JCSP.

The model is presented through an example: The Cash-Point Case Study. The main concept is STS, for Symbolic Transition System, that is a dynamic description coupled with a data type description. A Labelled Transition System or LTS is an automaton with simple labels, states are sometimes called control states in this document. STS add to LTS full data types, guards and input/output values. A transition is defined by `[guard] event communication / action`. The communication can be an emission (simple or multiple) or a receipt. The data types describe the operation semantics of the emitters, the guards, the generators and the actions. An algebraic style with positive conditional axioms is used to described the data types.

An architecture is almost a UML communication diagram rather than a UML component diagram: a link between ports denote event synchronization with communications. This was rather the KADL model which is dedicated to analysis and verification purposes: the communication glue is simplified which is very expressive in KADL and some other features are forgot: No inheritance between STS, ADT description are less general, No component parameterization.

The cash point example is illustrated on two points: defining a STS, defining the architecture. The existing elements or experimentations for tool support are explained.

- Defining an STS
  One first implementation was done Python (it is rewriting it in Java 1.5 under Eclipse) for the STS: the user writes a `.sts` file (a parser exists for the dynamic part), an interface generator builds an ADT skeleton `.adt` (the user has to fill the axioms), a Java translator generates a Java class from the ADT (experimental) but the user may also write a Java class or reuse an existing one.

- Defining an architecture
  A grammar allow to express component types, local instances, bindings and exports. This is a bit more complex since the parser and the loader have to be recursive. It also needs to call the STS parser and loader. When loading an architecture we have also to specify where are the Java classes. The instantiation process is also a bit more complex. It is not completely stable since some choices about bindings are pending.

The talk pursue by the level devoted to verification. In the past an approach using PVS (Theorem prover) has been experimented. The new approach rather close to model-checking is based on the notion of Configuration Graph (or CFG) which is a LTS but with a data value associated to each control state. This allows formal analysis methods to analyze component and their interactions. Currently the verification process is to compute the synchronous product and check it or to compute the configuration graph and prove some properties. This tackle the difficult problem of state explosion when model checking with data.

Then a Java implementation for it is presented (runtime interpreter) that relies on a rendez-vous mechanism to synchronize events between component protocols. This talk showed how to get a correct implementation of a complex rendez-vous in presence of full data types, guarded transitions and, possibly, guarded receipts.

A tool support is made of a library with parsers and analysis tools STSLIB is a project devoted to the design of sophisticated concurrent systems, their verification and code generation. Currently this is rather a Java API and the targetted runtime language is also Java. It may be used in the context of Eclipse or as a Java application.

Questions

One question was about the assistance for specification: only textual representations, the systematic usage of a 'self' parameter for data type operations, manual chain of specification processing. These point should be improved with the future (planned) GUI (Graphical User Interface) on Eclipse.

Another was about funtional (for specification) and imperative (for implementation) styles for ADT. Only a restricted form of ADT is accepted.

A last question was about communication primitives and especially the multiple one. Only the multiple rendez-vous is implemented.

Time and durations were also discussed.

COLOSS

Gilles Ardourel presented the Safe Components and Softwares (COLOSS - COmposants et LOgiciels SûrS) group, a team of Laboratoire Informatique de Nantes Atlantique (LINA, FRE CNRS 2729). The LINA laboratory in "Sciences and technologies of the software" is mainly a cooperation between Ecole des Mines de Nantes and the University of Nantes. It is specialized on two axes : distributed software architectures and computerized decision-making systems.

The research activities of the COLOSS Team range from Fundamental Research to Applications. The main goal is the elaboration of concepts, methods and techniques supported by tools for software designers and developers We explore formal approaches (based on mathematical foundations) to the assistance and the analysis in the development of software:

- Multi-formalism specification and analysis of software systems,

- Specification, verification and validation of components and software architectures.
  The goal is to ensure correctness of components used and their composition in complex software systems.

The motivations (fundamental challenges) are correct the software construction, the software quality and safety and the support for specific development methods. The main research areas are

- Multi formalism specifications, multi-faceted analysis.
  The monoformalism approaches are limited by a partial covering of problem and a partial analysis. COLOSS studies the Formal methods integration and faces some of its challenges (decomposition, semantic interoperability, formal analysis). Multi-platforms experiments are leaded on B, PVS, Spin, Grafcet, Petri nets...

- Design and verification of model properties.
  We investigate the use of formal methods and tools for modular modeling (component, objects) and property verification (system and model properties). In the CBSE context the motivation is to provide models

and practical tools to assist users in formal component-based development ; it covers the abstract definition of components and composition (simple, flexible and expressive), the verification of properties (safety, consistency, compatibility...) and the binding from components to code (refinement or code generation).

In the UML context the motivation is to improve the confidence in large and multifacet (diagrams) specifications where a formal semantics does not exist and where properties are quite complex *e.g.* model consistency is made of plenty (sub) properties. A single property can be decomposed into finer ones, can concern several groups of model elements, can be verified at different levels of completeness, and can be verified using several techniques with various costs and performances. We work on a generic verification process composite verification processes (with ordering, filtering, results propagation and annotation of faulty elements. . .) that supports the classification of verifications and properties (levels, diagrams...) and the abstraction of the results of different tools and formalisms.

COLOSS contributes to ECONET by providing Kmelia (an abstrat model) and its COSTO tool, its experience on formal methods and property verification, its experience on Java programming and a first experimentation on reverse-engineering Java to UML. COLOSS also expects fruitful exchanges with KADL and SOFA to both enrich these models and Kmelia.

The Kmelia Component Model Hierarchical Service Description and Analysis

Pascal André presented the Kmelia Component Model and the Property Formal Verification towards the COmponent Study TOolbox (COSTO: Component Study Toolbox). The COSTO modules are being developed with JAVA. The modules have been integrated into the Eclipse IDE as plug-ins. Both the specification and analysis were illustrated on a bank Automatic Teller Machine (ATM) Case Study and its withdraw service.

The presentation begins by an overview of the model. The Kmelia model is a simple, formal and abstract component model based on the description of complex services.

- The simplicity relies on the few number of concepts that are used to describe the components and their assemblies. The main characteristics are: components, services, software architectures, protocols, contracts, specification of complex interaction between services.

- The components are abstract, independent from their environment and therefore non-executable. Kmelia can be used to model software architectures and their properties, these models being later refined to execution platforms. It can also be used as a common model for studying component or service model properties (abstraction, interoperability, composability).

- A formal model is defined for components and services, including syntax and type checking, contracts and LTS. This is the basis for any automated processing support.

- The service are first class elements and not only messages which means that (1) services may be defined by a dynamic behaviour (a protocol) in addition to their signature and contract and (2) services may be composed of other services. Each service has an enhanced service interface which includes a service dependency composed of the provided services and the required services which are used in its context. The notion of service is central to Kmelia and a component interface describes mainly services. This means that the components are connected via their services (functional connections). The interaction model is therefore simple: required services are directly linked to provided services.

The model is extensible: it is possible to add new concepts from the kernel *e.g.* protocols, adaptors, aspects...

A component is a structuring unit that encapsulates a state and services; has an interface with usage constraints. A component has an interface including provided services and required services. A service is an abstraction of a functionality (signature, contract); it has a behaviour (dynamic evolution). The behaviour is defined by an extended LTS where transitions accept functional actions or communications (messages send or receipt, services call or return). A service also has an interface including provided services and required services that may lead to a hierarchical structuration. Assembly links are a simple support for component connection and interaction. Promotion links are a means for simplifying encapsulation.

The second part of the talk is dedicated to the verification of properties and its support with COSTO. The verification principles are: a formal verification of properties, a reduced user interaction (automation), customisable verification process, interconnection with appropriate tools (open framework). For this last point, the specific property analysis modules is realised by COSTO modules and the general property analysis are checked by connection with existing tools (Model-checking or theorem-proving). This is illustrated on the ATM example for the

behavioural compatibility property. Its scope is correctness of components and assemblies compositions availability of components and services, the compatibility of linked interfaces, the service compatibility and a diagnosis on mismatch. The service compatibility is defined at four levels of control: service signatures, enhanced service interfaces, contracts (pre/post conditions), and the behaviours (correct interactions between the caller service and the called service via the required service). An illustration with MEC an existing powerful model-checker for synchronised LTS. MEC has a compatible definition of STS that allows a Systematic translation into input formalisms and feedback. A verification is done for each triple of service based on an first level assembly link. In the ATM example, a deadlock is detected and visualized.

Questions

   Questions were asked all along this talk.

2.2   Working Session

2.2.1   Introduction

The goals of the working sessions are mainly to fix a battle plan for the project. This means to clarify and delimit the detailed objectives in a feasible manner, to define clearly the concrete and coordinated contribution of each partner, to define task, products and results, to organise tasks (responsabilities, contributors, schedule...). One first issue was to check that each partner has a compatible (or even better the same) vision of the project. Another one is to structure the work in several parts because a monolithic task cannot be realised by four geographically separated entities.

   These sessions are highly dynamic so that a strict program was not established. Thus the detailed program given in the following subsections are post-workshop programs. Nevertheless the initial program covering included three aspects for the working session

- decision on the source and target area boundaries

- discussions on the way to get reverse transformations

- find application examples

In the following we will summary the session contents day by day.

2.2.2   Wednesday, September 5, 2007

This is a half-day session.

Time	Activity	Speaker
09:00	Introduction to the working sessions	Pascal André
	LCI Point of view	Dan Chiorean
	DSRG Point of view	Frantisek Plasil
12:00	Round tables and discussions	all

Introduction to the working sessions

The slides of this (first plan) introduction are provided on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07
   Pascal introduced the working session by giving its view of the current project situation and provided some tracks for the discussions. Consequently the Working Session Roadmap was organised around four points

1. Convergence on the objectives

2. Convergence on the means

3. Definition of the tasks

4. Production

The three first points define an initial objective for each day of the working session.

1- Convergence on the objectives

There is a clear agreement on the "abstract" context

1. We have abstract component models (SOFA, Kmelia, KADL) and tools. Some have elements for Java code generation (engineering).

2. We assume Java Code Specifications and Programs. This code contains informations about structure and behaviours (even if it was not designed in a CBSE).

3. We want to practice Reverse Engineering = from code to abstract models. There exist general techniques and tools such as code analysers, patterns, extractors...
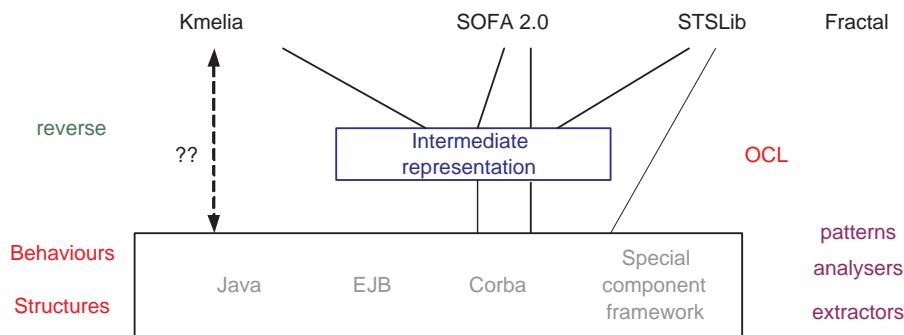
Figure 2.1: ECONET Project: "abstract" context

There is a fuzzy vision of the "concrete" context.

- What sort of Java code nature do we want to process ?

  - Bytecode - the one that exists at run-time.
  - Plain source - the one usable for an open source project.
  - Annotated Source - a Java program (source or compiled) that includes informations usable for connecting to an abstract model. This is the case when the program is (aprtially) generated from a component abstract specification by some code generator.

- Assuming we have a Java program, does its code be structured in some way ?

  - plain Java: there are no special structure.
  - "componentised" Java (EJB, Corba, .NET, issued from a code generator - SOFA, Fractal...): the code has a component flavour.
  - "behavioural" Java (threads, communication primitives, issued from a code generator...): the code has a behaviour flavour.

- What are the reengineering issues ? It means toward what objective do we look for abstractions ?

  - legacy code recovery/discovery: the usual goal of reverse-engineering. You have an existing code but it was not designed according to the same abstract concepts.
  - compare code and specifications (conformance): we want to check whether a concrete representation and an abstract representation (of the same system !) are consistent.
  - roundtrip: the code is generated from a model and complete that model. The development process includes engineering and reverse-engineering activities. This approach is frequent in MDA.
  - ...

Goal of day 3 = Clear agreement on the "concrete" context

2- Convergence on the means

Once we agree on the right objectives, the question is how can we reach them.

- Collaborative State of the Art Study.
  The idea here is to share the individual experiences both on domains and tools in order to accelerate the collaborative work. The state of the art covers works and tools related to: reverse engineering (in general, for components), java reverse engineering, Java code manipulation (analysis, annotation, extraction...).

- Re-engineering techniques.
  The objective is to be up-to-date in techniques available in the open source community.

    – Java Compilers and Analysers to handle the Java code.

    – Patterns, rule based systems for reverse-engineering issues.

    – Used notations and Intermediate layers (models) for a step-by-step process.

    – ...

- Separate modules (e.g. structural / behavioural / metamodels). Do we work on separate parts and levels with different applications or shall we use one program only ? Do we propose a framework for general purpose or do we design a single purpose application ?

- Benchmark example.
  It is quite difficult to find Java code related to components. A collaborative work imlies that we work together on the same Java program.

We also have experience on OCL and metamodels.
(optimistic) Goal of day 4 = organise the means tracks and find the benchmark

3- Definition of the tasks

Once we agree on the right objectives and we have a somewhat clear idea of the means we provide, the question is to organise the work *i.e.* the project management.

- What to do ?
  Define a list of tasks to do and the delivery.

- Contributions ?
  Define the actors and collaborations (who do what).

- Synchronisation points
  Point out the botlenecks of the process.

- Planning
  Define who do what and when.

- ...

(optimistic) Goal of day 5 = each participant has a somewhat clear idea of what he will do

4- Production

Here are some guidelines for that part.

- Workshop Report

    – Collect paper and slides

    – Summary of the discussions

+ Bibliographical Notes

$\implies$ project plan for year 2 and *Evaluation*

- Fix the participants objectives

- Documentation, research reports

- Intermediate results $\implies$ Second Workshop

- Publications (?)

These are also part of the initial 'Second year objectives'

LCI Point of view

Dan talked about various aspects of reverse engineering and especially those related to models (MDA). Reverse engineering is about finding the concepts of the models by analysing the code and inject the model. There are several models: the programming language (Java) model, the component models. He stressed three aspects:

1. About reverse engineering (RE).
   He expects a more clear defintion of the context and goals. The domain is quite difficult and unexplored. There are different programming languages (Eiffel, C++, Java...) and different tools. There are some existing work on the subjet, we should have a look at them. He worried about the practical result of Component RE (the value of RE, the mandatory results). Do SOFA applications have non-SOFA parts.

2. About Behaviour Checking.
   Do we want to detect possible deadlocks in the code ? What can we do if component instances relate to the same component type.

3. About per/post conditions.
   This information is rarely present with a clear format in programs (even for java assertions).

Dan also noticed that most tools work with the architecture of the application. He also remind that LCI had a previous experience with reverse-engineering UML from C++ programs.

DSRG Point of view

The slides of this intervention are provided on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

Frantisek recalls that the ECONET project is a small project (budget, duration). He commented the initial project definition (see section 1.1). As a general remark he recalled that the pragmatic outputs of the project are a cross fertilization and joint publications.
He distinguished four area of contribution by DSRG are:

1. Providing SOFA.
   This is one of the abstract models. Providing covers the language definition, the tool support, examples, the knowledge..

2. Enhancing EBP by additional features from STS/eLTS (to enhance expressive power of EBP). The idea is to take concepts from other abstract models..

3. Extracting protocols (EBP) from code (at least BP from code). This is a part of the reverse engineering if we consider that there are two complementary aspects in a program: the structure and the behaviour..

4. OCL: WFR (well-formed rules) for component diagrams, activity diagrams? WFR for SOFA components, The idea is to improve the SOFA language definition using the LCI experience..

Note that point 2 and 4 are indirectly related to reverse-engineering but can participate to define a more general framework.

Frantisek brings an answer to one of the *means* issues: the benchmark. He proposes to take an exemple from a previous work on a Dagstuhl Modelling Contest, the CoCoME (Common Component Modelling Example) http://www.cocome.org/ The advantage is to benefit from a Java program that implements a component application (this is rare enough to have both a specification and a program) and two abstract models that have been manully built on it. Two trivial examples are more fruitful.

Discussions

The discussions took place from the three above interventions.

Here are some Gilles's random ideas:

1. We're interested in behavior models.

   (a) Getting them from plain java code without structural information about the component architecture is far beyond the scope of this project.

   (b) We at least need some kind of "boundaries".

2. There are two first steps :

   (a) Our benchmarks shouldn't be spagetthi code (while not sticking to a particular comp model, the application should have a something that look like a component architecture). A proposed task is finding fitting benchmarks.

   (b) Then let's pretend we already got most of the structural info about the component architecture (Manually, Automatically from Specification or code...).

3. We're about to target different abstract models.

   (a) We need meta information somehow common to the models : a minimal structural component model (component hierarchy, one or several interfaces by component). A proposed task is finding this minimal meta information. We don't need a Unified Component Language -just the minimal stuff to work (remember the size of the project).

   (b) Additional model-specific meta information (because we might want to do something beyond this project's scope some day).

Annotations examples

```
code source part (class, package, method, whatever..)  "is part of componet id #12344"
code source part (class, package, method, whatever..)  "is part of interface id #12344"
```

Here is a short summary.

- Benchmark
  The CoCoME benchmark is adopted. It has many interest: existing informations on requirements, on component design, available Java program.

- Input
  Of course the fine input would be a component program but it does not exist yet. A better input is an object program with informations (commments, annotations) on components. Again it does not exist yet. We decidede that the input is a plain source Java with possible annotations or informations (comments, separate documentation files, UML models...). Having a source code allow to use parsers, extraction techniques, comments and various informations that are not available for java bytecode. These annotations and/or informations should inform on some component abstractions. We are not sure to find such informations but we assume that as soon as component design will be applied (later), it will help to inject annotations in the plain Java code.
  A practical question remains: which informations or annotations do we need ?

- Input Model
  We do not want to have a instantiated model of the Java programs *i.e.* we carry only some informations of the program not all of them (no model transformation for instance).

- Reverse engineering goal
  The goal is to find abstractions than realise full reverse engineering. The abstraction help to analyse some properties of the program, to get an abstract model, either to compare with an existing one (conformance) or to document the application.

- Separation of concerns
  We discussed about two main problem issues: structure/behaviour, one or more target.

  – We distingues the structural informations from the behavioural ones. Both aspects are orthogonal and existing works mainly relate to one aspect only. Moreover finding the behavioural abstractions depends on both the Java code and the structural model. The structural part is at first a delimitation of what are the components in the Java code.

  – We may want to target several abstract model. Shall we implement a tool for each of them or try to share both procedure and knowledge. Note that some of these model can have only structural features. One way to proceed is to use metamodels (pending issues to be studied on thursday).

  This separation of concerns also make it easy to split the project realisation.

- Background
  Other contributions of the project are recalled: cross LTS extensions, WFR definitions.

2.2.3 Thursday, September 6, 2007

Time	Activity	Speaker
09:00	Welcome	Pascal André
|	CoCoME Contest	Jan Kofron
|	"ECONET tasks" proposal	Petr Hnetynka
|	CoCoME in SOFA 2.0	Jan Kofron
12:00	Round tables and discussions	-
13:30	Java Path Finder, Bandera	Jiri Adamek
|	Project modules and interfaces (discussions)	-
17:00	Toward an annotation language (discussions)	-

Welcome

Pascal introduced the second day working session by giving an abstract of the discussions and the gainful decisions of the first day. Since the goal of day 3 were roughly reached we can now fight the ones of day 4, which are convergence of the means (see section 2.2.2) and especially try to advance on the input format, the benchmark and the available techniques. To this end a presentation of the benchmark has been planned in the morning and another one on Java Path Finder (and Bandera) in the afternoon. The rest of the day (morning and afternoon) is reserved for discussions.

The slides (second plan) are provided on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

CoCoME Contest

Jan Kofron presented Common Component Modeling Example contest.

CoCoME http://agrausch.informatik.uni-kl.de/CoCoME is an international contest aiming at comparison of different modeling approaches on a common assignment organized by Technische Universität Clausthal, Universität Karlsruhe, Politecnico di Milano, Charles University in Prague. Sharing a common close-to-real-life assignment should reveal strengths and weaknesses of the different modeling approaches and thoroughly test their applicability in practice.

The assignment is a trading system, a Business application for managing chain of stores with about 200 stores and 8 cash desks per store. Participants had as input a partial specification via UML, informal specifications as use-cases for the requirements, deployment diagrams for the distribution and a reference implementation (realised by a teacher and students).

The workshop held at Dagstuhl in Germany. Eighteen teams were involved in that contest. The objectives was to model the application on various aspects (structure, deployment, behaviour modelling extends behaviour protocols). Note that Uml models were not fully consistent with the code and the components were given.

The slides of this intervention are provided on the Workshop Wiki at:

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

"ECONET tasks" proposal

Petr Hnetynka proposed some answers to pending questions on the objectives and the means.

To the question "What is an input?" his answer is "plain Java".

For obtaining a behavior specification we need also a model (i.e. architecture). To the question "How to obtain model?" his answer is "it is already given or we extract it from the plain Java sources.

We need a single concrete meta-model (for specifying models). The problem is that each component system has its own means for specifying models but most of component systems are similar (black-box component, provided/require services, nesting), thus we can use the core of the SOFA 2 meta-model as a concrete meta-model.

In advance to the objectives of the day he also proposed a first task assignment proposal: model extraction (-?) and behavior specification extraction (DSRG).

The slides of this intervention are provided on the Workshop Wiki at:

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

CoCoME in SOFA 2.0

Back to the CoCoME, Jan Kofron overviewed a SOFA solution for the Common Component Modeling Example. He skipped the SOFA 2.0 presentation to focus on the general design solution and the verification of a part of the system.

The following summarizes modeling of the CoCoME assignment using SOFA 2.0.

- Architecture
  First, the SOFA architecture of the TradingSystem (Fig. 2.2) was created, forming basis for further work.

- Behavior modeling
  All CoCoME components were specified using EBP. Parts of the CoCoME assignment are ambiguous and even contradictory ; the behavior specification is mainly based on the reference implementation.

- Verification
  Checking communication of components for errors (Bad-activity no-activity (deadlock) infinite-activity), Compliance checking (Translation of EBP specification into Promela using the SPIN model checker to search the state space),
  Code checking (Via combining Java PathFinder with Behavior Protocol Checker).

- Performance

Benefits : As SOFA 2.0 explicitly supports components throughout the entire software lifecomponent cycle, the architecture erosion is mitigated. Moreover automatic connector generation provides seamless component distribution. Behavioral modeling via EBP and subsequent verification allows for reasoning about correctness of the design even before actually having an implementation. Then with the implementation available, one can use code checking to find out, whether the implementation obeys its EBP specification.

The solution involves (i) modeling architecture in SOFA meta-model, (ii) specification of component behavior via extended behavior protocols, (iii) checking behavior compliance of components, (iv) verification of correspondence between selected component Java code and behavior specification, (v) deployment to SOFA run-time environment (using connectors that support RMI and JMS), and (vi) modeling of performance and resource usage via layered queuing networks. We faced several issues during implementation of the CoCoME assignment in
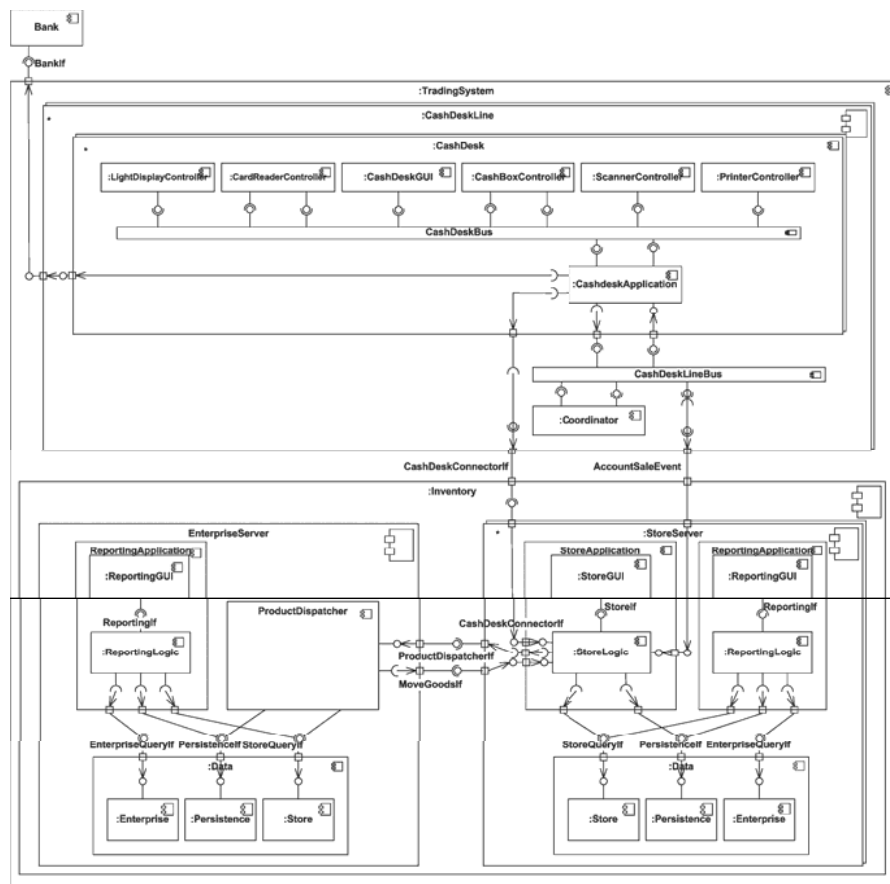
Figure 2.2: CoCoME: Sofa static architecture

SOFA 2.0. Most notably, the architecture was modified in order to improve clarity of the design Ű in particular, the hierarchical bus was replaced by two separate buses and the Inventory component was restructured. Extended behavior protocols for all the components are based on the provided plain-English use cases, the UML sequence diagrams, and the reference Java implementation (the assignment does not include a complete UML behavior specification e.g. via activity diagrams and state charts).

Jan showed the SOFA solution for CoCoME: static view, behavior view (BP [sofa, fractal], EBP [Sofa 2.0]), deployment view, performance view (model performance), comparison with UML, flat / hierarchical models, multiplicity, evaluation.

The slides of this intervention are provided on the Workshop Wiki at:

http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

Morning Discussions

Here is a short summary of thursday discussions and decisions.

Petr drew a quick picture of a Sofa metamodel (Fig. 2.3) which should correspond to all our component models (with different names).
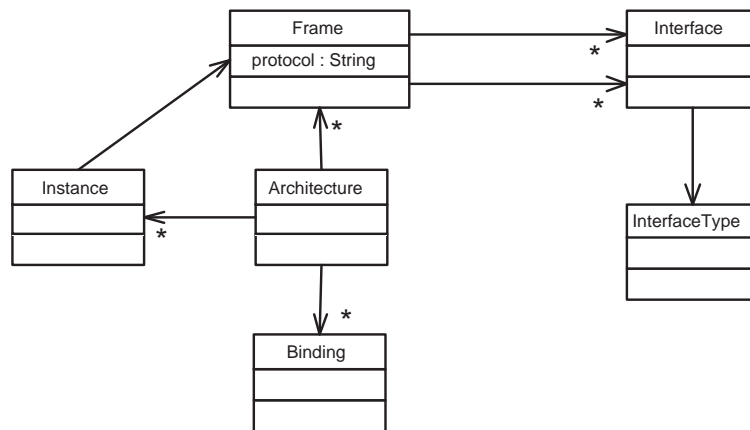
Figure 2.3: Sofa: short metamodel

Then we discussed about the application design (Fig. 2.4). This figure shows a three parts architecture with a common metamodel and a dependency between structural and behavioural abstraction. The contribution is an annotation definition, a first prototype (Java analyser, xDoclet) that must be incremental. See also Spring anf Fractal.
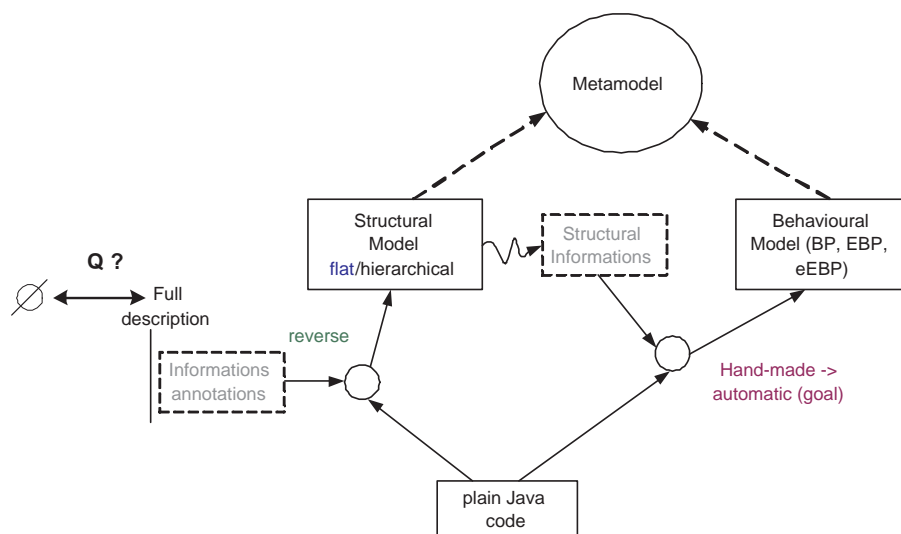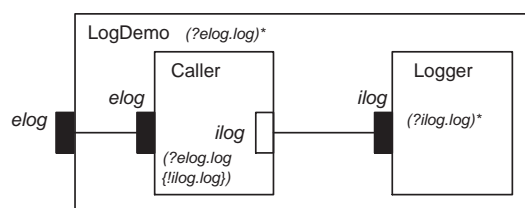

Figure 2.4: Econet Architecture: draft 1

After, Tomas explained a short example of code generation from SOFA architecture. He took the `LogDemo` example and illustrated different versions of the Java generator.


```
module logdemo {
  interface LogInterface {
    void log(in string number);
  };
  frame Caller {
    requires:
      LogInterface Log;
  };
  frame Logger {
    provides:
      LogInterface Log;
  };
};
```

There are two primitive components - `Caller` (or `Caller` in his example) and `Logger`. `Logger` provides the interface `LogInterface`, `Caller` requires this interface, in the component `logdemo` these two components are instantiated and their provision and requirement are bound together.

In the first translation, plain Java code is generated with a specific implementation of requirements (variable + methods).

```
 // LoggerImpl.java
package SOFA.demos.logdemo;

public class LoggerImpl implements LogInterface {
  public void log(java.lang.String msg) {
    System.out.println("**** LOG: "+msg);
  }
}

// CallerImpl.java
package SOFA.demos.logdemo;

public class CallerImpl implements Runnable {
  SOFA.Component.DCUP.DCUPComponentManagerImpl cm;
  LogInterface LogRequirement;
  boolean end;
  boolean stopped;
  public CallerImpl(SOFA.Component.DCUP.DCUPComponentManagerImpl _cm) {
    ...
  }
  private void setRequirement() throws SOFA.Component.NamingException {
    LogRequirement = (LogInterface) cm.getRequirement("Log");
  }
  public void run() {
...
  }
  public void setEnd() {
  }
  public boolean isStopped() {
  }
}
```

In a second translation, the provisions and requirements are specified as annotations in Java for a post processing.

```
 // LoggerImpl.java
@provision(log)
public class LoggerImpl implements LogInterface {
  ...
}

// CallerImpl.java
@requirements(log)
public class CallerImpl {
  boolean end;
  boolean stopped;
  public CallerImpl() {
    ...
  }
  public void run() {
...
  }
  public void setEnd() {
  }
  public boolean isStopped() {
  }
}
```

The architecture is generated or predefined. There are templates for connectors.

Last, we discussed about flat component / hierarchical components and the composition operators. AT first we cannot reengineer the composite components from scratch. One can build hierarchies on performance criteria or distribution criteria. But is is enough for instance to build the primitive components and then manually to build the structures. Jiri proposed launching the code with introspection rather than parsing the code.

Java Path Finder, Bandera

The afternoon started with a short description of JPF (and Bandera) by Jiri Adamek. The slides of this intervention are provided on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

Bandera

Jiri explained that Bandera was unstable at the time he studied it and the second version was not really public on the web site[2]. Bandera is a 6-years old source transformer. Bandera allow to check in a semi-automatic way different properties for different abstractions (Fig. 2.5). Two years ago a model-checker for Java, called BOGOR[3], has been developped ; it can be a future source of information and tool.
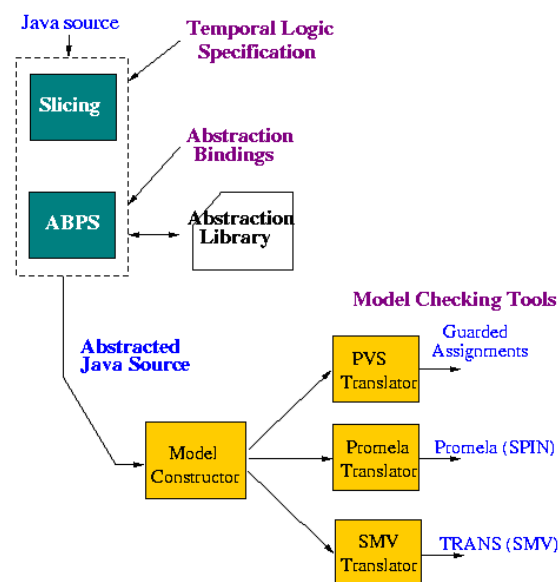
Figure 2.5: Bandera overview

Java Path Finder

JPF is a Java model checker developped at NASA Ames Research Center[4].

In general, JPF is capable of checking every Java program that does not depend on unsupported native methods. The JPF VM cannot execute platform specific, native code. This especially imposes a restriction on what standard libraries can be used from within the application under test. While it is possible to write these library versions (especially by using the Model Java Interface - MJI mechanism) there is currently no support for java.awt, java.net, and only limited support for java.io and Java's runtime reflection. Another restriction is given by JPF's state storage requirements, which effectively limits the size of checkable applications to 10kloc (depending on their internal structure) if no application and property specific abstractions are used. Because of these library and size limitations, JPF so far has been mainly used for applications that are models, but require a full procedural programming language. JPF is especially useful to verify concurrent Java programs, due to its systematic exploration of scheduling sequences - an area which is particularly difficult for traditional testing.

One problem in a component view is that it accepts only closed java programs. It explores the state space. Out of the box, JPF can search for deadlocks and unhandled exceptions (e.g. NullPointerExceptions and Assertion-Errors), but the user can provide own property classes, or write listener-extensions to implement other property

---

[2]http://bandera.projects.cis.ksu.edu/
[3]http://bogor.projects.cis.ksu.edu/
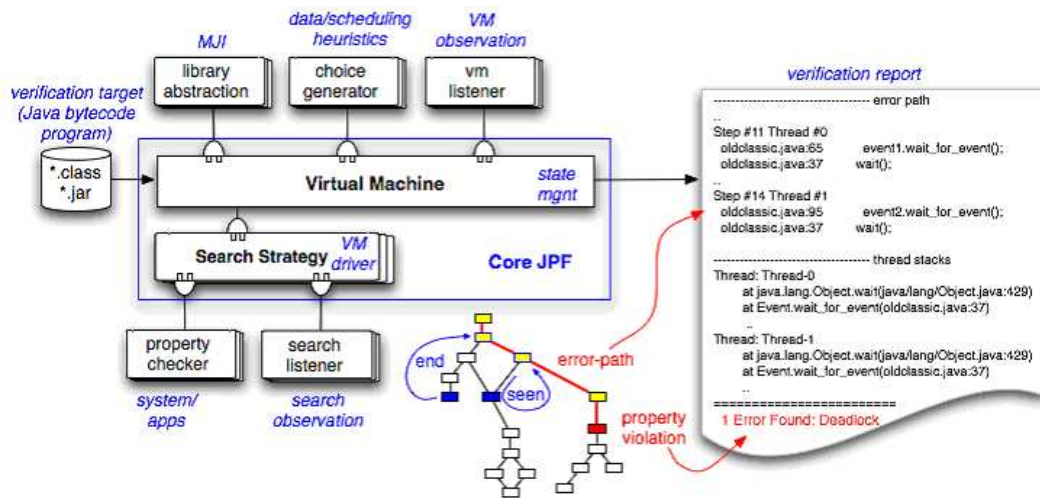[4]http://javapathfinder.sourceforge.net/

Figure 2.6: JPF model of operation

checks. A number of such extensions, like race condition and heap bounds checks are included in the JPF distribution. One advantage of JPF is that it accepts **listeners** for custom search algorithms and an access to a Model-Java interface via its API (Fig. 2.7).
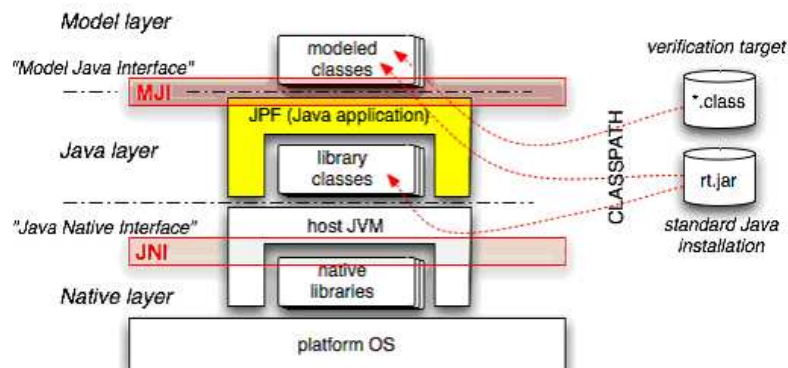

Figure 2.7: JPF Java layers

Project modules and interfaces (discussions)

In this part we discussed more precisely on the project architecture (Fig. 2.8), its modules and the minimal information we need for behavioural abstraction.
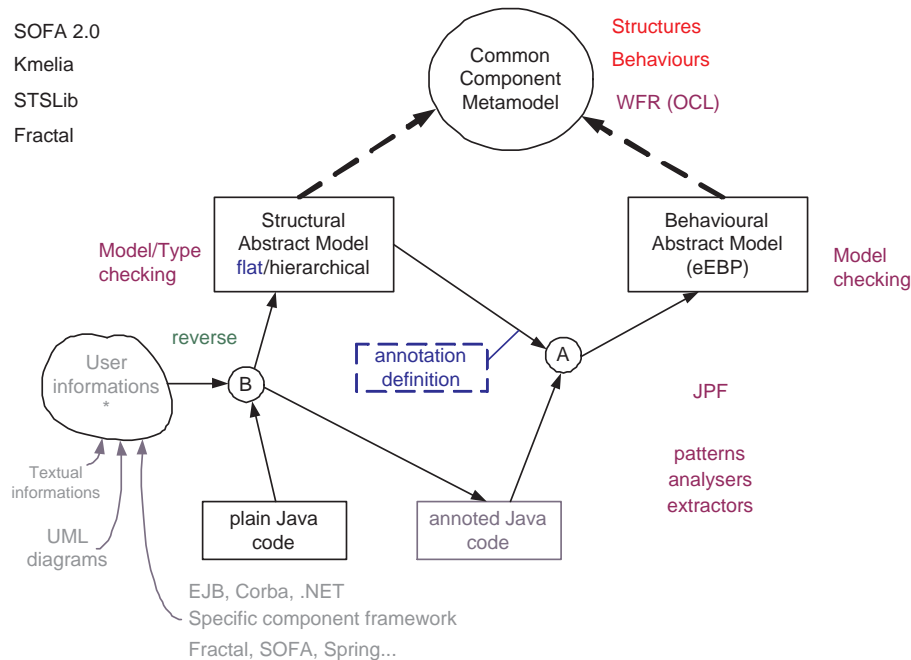
Figure 2.8: Econet Architecture: final version

The abstraction process ((Fig. 2.9)) should infer components and structure for the behavioural abstraction. The information are given directly by the source code via Java annotations. A main information for the latter is the "entry point" information (intialisation). The entry point denotes the active component.
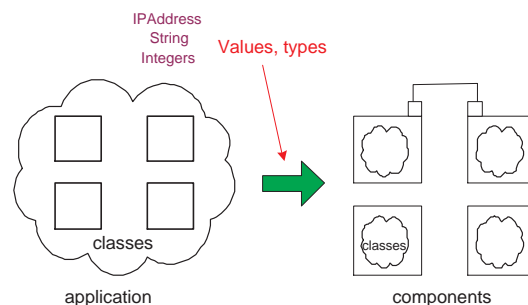
Figure 2.9: Abstraction Process

The B process is more general than the A one since it can apply to non-behavioural abstract models. *It provides more complete results with less informations*. The borders delimit frontiers and component contexts (separate business and management for example) and link classes to components. The goal is to extract interfaces (one or more) using hierarchical graph analysis for example. The approach uses annotation based methods and techniques coupled with user friendly graphical interface to get the missing informations from the user. Possible starting points and resources are: Java only, UML models, component models, EJB program...

Toward an annotation language (discussions)

To define the required informations (and annotations) we tried to map the model concepts (Fig. 2.10). We separate business functionalities from non-business ones (related to Java computations or to management handling e.g.

setRequirements). We should find annotations about provided interfaces (which is absent or difficult to find in plain Java). Provided interface are annoted on java interfaces and methods. Required interfaces can be annoted from java attributes. All the class belonging to a component should be annotated. One question is: how many instances of a component do we accept, only one ?
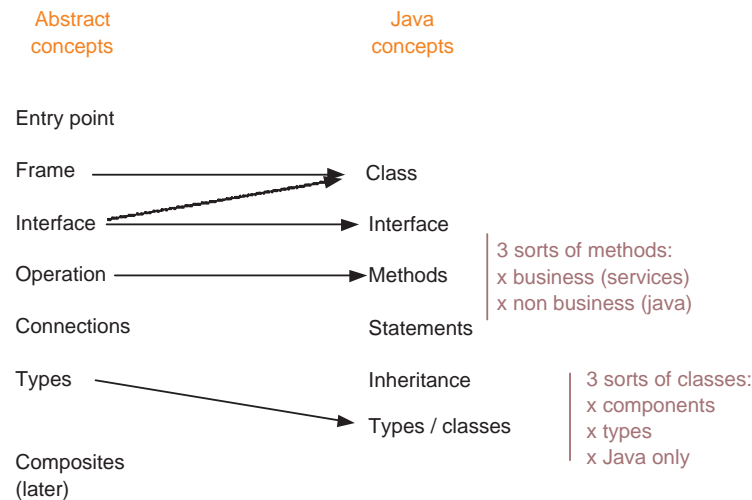

Figure 2.10: Mapping concepts

Remark 1: there is one protocol per component (frame) and not per interface.
Remark 2: the annotations must be complient with other java annotations (meta model).

2.2.4   Friday, September 7, 2007

Time	Activity	Speaker
09:00	Welcome	Pascal André
|	Comparison of Abstract Models (discussions)	-
|	Common Meta Models (discussions)	-
12:00	Round tables and discussions	-
13:30	Technical discussions (RE techniques and tools,	-
|	annotation Language, collaborative tools...)	-
17:00	Closure	Pascal André, Ondrej Sery, Petr Hnetynka

Welcome

Pascal introduced the third day working session by giving an abstract of the discussions and the gainful decisions of the previous days. We significantly progressed on the project structure and module interfaces and reached partially the convergence of the means (see section 2.2.2). This can be summarised as follow

- DSRG experience - CoCoME, Behaviour Extraction, Tools (JPF, Bandera)

- Project Architecture (Fig. 2.8) Three parts

  1. Component Metamodel cross LTS extensions, WFR

  2. Structure Abstraction user interacted tool

  3. Behavior Abstraction A-interface definition, annotations generation

- Problem Domain Restriction

  – metamodel $\implies$ components and behaviours

  – A $\implies$ no connections, no composition, no statement abstraction

– B $\implies$ no composition, no statement abstraction, user-interactions

- Benchmark = CoCoME

There remain things to do for this last day is to progress on the tasks definition, the responsabilities repartition and the planning building. Moreover some discussions were mandatory on technical informations: models (abstract and concrete models toward a common meta model), techniques (control flow or parsing, detailed annotation language, ...). The (optimistic) goal of day 5 is taht each participant has a somewhat clear idea of what he will do later.

Here are some guidelines for the definition of the tasks:

- What to do ? on the draft architecture

    – Metamodel

    – Process A

    – Process B

- Contributions ? a subset of

    – Common Metamodel definition ?

    – Annotation language definition (input of process A)

    – Tools Prototypes for Metamodel verification, Process A, Process B

- Synchronisation points =
  A-interface, Metamodel def, B-Information def

- Planning deadlines

    – Evaluation (october 2007)

    – Workshop Nantes (begin of March 2008)

    – Workshop Cluj (end of august 2008)

- ...

The slides (third plan) are provided on the Workshop Wiki at:
http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:pragues2007:program07

We started the day by a Comparison of Abstract Models and after several discussions on the Common Meta Model,the Annotation Language, the tools and the task repartition.

Comparison of Abstract Models (discussions)

The discussions continued with a fast comparison of the three abstract models (SOFA, KADL, Kmelia) in order to grasp the structural and behavioural models and therefore tha annotations and some kind of metamodel.

Concept/Model	SOFA EBPL	KADL	Kmelia
Attachment	Frame	Component	Service+ component
Operations **(computation)**	atomic assignments (constants?)	atomic functions (algebraic)	atomic action+ service calls
Types	Enums	any ADT	"complex but open" means ad hoc
Guards	logic + enum	logic + ADT	logic + ad hoc FL
Dynamic formalism	reg. expr.	state transition	state transition + "hierarchy"
I/O	! ?	? ! *	? ! ?? !!
Labels	?iface.notified {!iface2.pre}	[guard] event com/action	[guard] action* (actions can be com or functions)

We studied the corresponding java constructs in a engineering/reverse-engineering points of view.

Concept/Model	SOFA EBPL	KADL	Kmelia
Attachment	set of classes	set of classes	set of classes
Operations (computation)	plain methods user Java statements	plain methods algebraic translation	methods + behaviours generated code
Types	Java types	Java types classes (ADT)	Java types + classes
Guards	boolean expr.	boolean methods	conditions
Dynamic formalism	control flows (RMI...)	control flows (LTS Library)	various statements (control structure, messages, methods))
I/O	method calls parameters	method calls parameters	method calls parameters
Labels	assignments user Java statements	if-then-else patterns	statements (Kml-lang)

We started the day by a Comparison of Abstract Models and after a discussionson the Common Meta Models and the Annotation Language, and the task repartition.

Common Meta Model (discussions)

The common component meta model will include only the common part in its first design, leaving some holes for specific features. The structural concepts are quite similar in the target languages. The main features are those of the annotation language. They differ mainly on the representation of behaviours (Fig. 2.11).
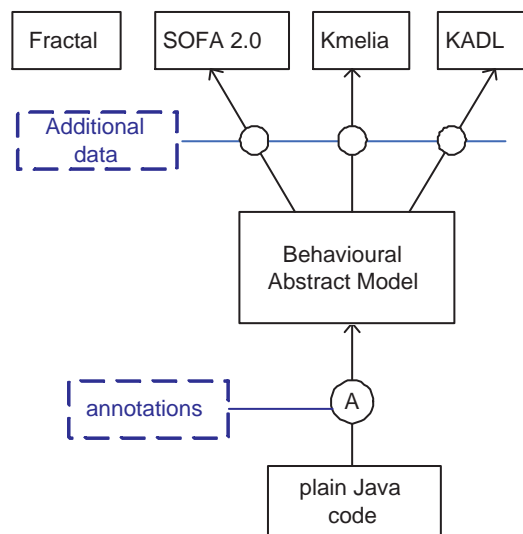

Figure 2.11: Common Component Meta Model

Technical discussions

First a debate opened on the behaviour abstraction RE techniques and tools. On one the usual way to proceed is to build the control graph: the states are the steps in the system evolution - its state space - and the transitions are the events and actions performed (method call, statements, control structure). Then we can split the system behaviour onto components or just model check it. The problem is that it contains low levl informations and it should be abstract: group state and transitions. This technique is well fitted to LTS base components. Another technique is to parse the java code and build parse trees (AST abstractact syntax tree with terminal and non terminal tokens). This provides modular trees that can be merged/collapsed/extended to a more general flow. This technique seems to be interesting to build regular expressions for the SOFA model. It uses the SunAPI for Java and tools like `javacc`, `antlr`, `JavaCup`, etc. In any case, one has to cross the results with existing component structures. The second solution is chosen.
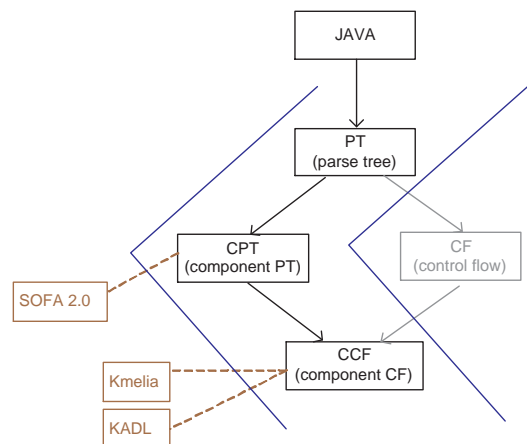
Some issues remain open:

Figure 2.12: Java Behaviour Abstraction

- Parse Tree

  - Coloration used to collapse parts of the Parse Tree

  - Tools ?

- Control Flow

  - Coloration used to collapse parts of the Control Flow

  - Tools ?

  - Format ? textual +API

- Find a good common format for both parse tree and control flow.

Then we exchanged views about the annotation language. The **constructs in the component model** are associated or derived from one or several annotations in the code.

Frame/Component

- Corresponding Java construct: One or more classes

- Corresponding java annotation:

  - target class or interface `@InComponent(annotation_source, name_of_the_component)`

Interface

- Corresponding Java construct: one or more interfaces, or a set of methods from a class

- Corresponding java annotation:

  - target class `@provided(annotation_source, name_of_the_model_interface, name_of_the_ja`

  - target method `@provided(annotation_source, name_of_the_model_interface)`

  - target attribute `@required(annotation_source, name_of_the_model_interface)`

  things to think about later : (temp requirement passed as parameters Ě)

"entry point"

- Corresponding java construct: The main class of a component

- Corresponding java annotation:

    – target class `@initclass(annotation_source, name_of_the_component)`

    – target method `@initmethod(annotation_source, name_of_the_component)`

    Gilles: `name_of_the_component` seems redundant because the method or the class should already belong to a component (see InComponent annotation)
    things to think about later: (consistency rules needed? is having several initclasses, methods in a component legal ?)

Operation   Do we need it ? We already know methods from the provided annotation

- Corresponding java construct: Method

- Corresponding java annotation:

    – target method `@`

    Business methods have to be singled out

Types   Goal: identify parameters of business methods
Problem: String or int may be used as business object sometimes and sometimes not

- Corresponding java construct: class or interface

- Corresponding java annotation:

    – target class or Interface `@initclass(annotation_source, name_of_the_component)`

    – target method `@ businessparameter(annotation_source, parameter_name)` (this one takes precedence over the previous one)

    things to think about later: another annotation could be for abstracting data : telling that an integer only has a few cases...

Business Types have to be annotated. In some cases (library without accessible source code) they can't be, so a textual configuration file will be used to list them or to list non-business types. White list, black list, regular expressions over full name ?
    Last we disccued about tasks, responsabilities and milestones. Every body participates to the common annoted bibliography on the wiki. Here is a pictured summary of the responsability and participant repartition on the project structure (Fig. 2.13).
    As a collaborative tool, a version manager (CVS) will be installed by DSRG, in addition to the wiki plateforme installed by COLOSS. We aim share the tools (source and experience) used during the development.

Closure

Production

- Workshop Report

    – Collect paper and slides Please send them to me

    – Summary of the discussions

+ Bibliographical Notes

$\Longrightarrow$ project plan for year 2 and *Evaluation*
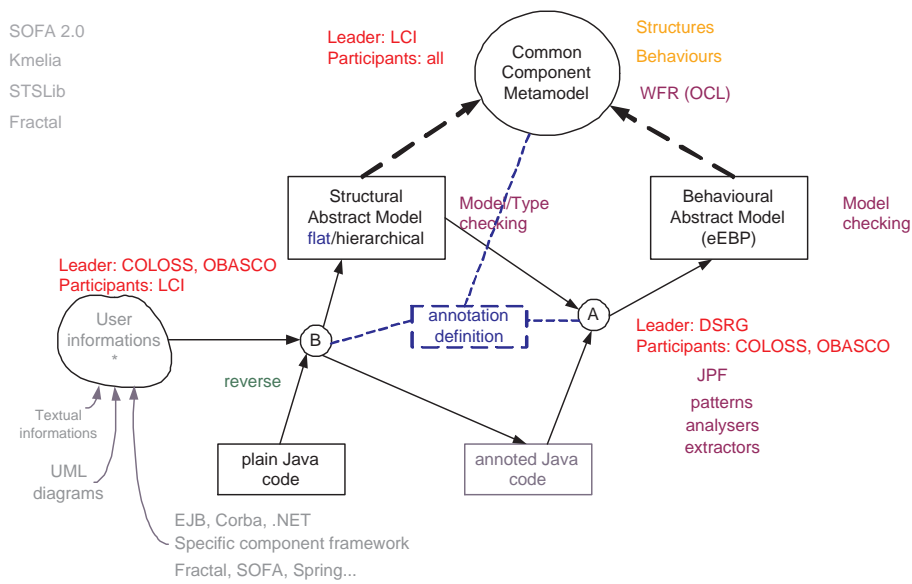
Figure 2.13: Econet Architecture: responsabilities

- Fix the participants objectives

- Documentation, research reports

- Intermediate results $\implies$ Second Workshop

- Publications (?)

see also the initial 'Second year objectives'

The organizers thanks the participants for their contributions and take a date for a next workshop at Nantes in 2007.

Chapter 3

Project Architecture

The contents of this chapter has been defined individually after the workshop. It presents a detailed vision of the three subprojects, defined in the workshop. There remain some coordination to process on the common parts (especially on the interface formats and model tools) and also to share the experience on tools.

3.1 Structural Abstraction Subproject

Writer: Pascal André, Gilles Ardourel

This is currently a draft version.

3.1.1 Objectives and Goals

The objective of the process B (Fig. 2.8) is to build a structural component model and a corresponding annotated java code. These two elements are inputs of the process A (see the detailed process in section 3.2). The model is also an instance of the metamodel (see the detailed description in section 3.3) that will control its consistency.

A general view of the process B is given in figure 3.1; from plain Java code and user interaction, process B should produce an annotated Java code and a corresponding component model (both results must be consistent). Some restrictions apply to the first program release:

- Input

  - Annotations are those related to the common Component Meta Model (CCMM) but do not include other component models yet (Fractal, Sofa, ...). The latter will be called **extended annotation**.

  - UML models are not accepted as direct inputs but are read by the user.

- Output

  - Only flat component model are targetted.

  - Process B is not directly responsible of the consistency between a model and the corresponding Java annotated code.

  - The conformance of the produced component model is checked at the metamodel level.

The process B is in fact iterative because its source is variable (in the sense that it may include many informations from different nature) and target different goals. For instance one goal is to abstract structural elements of a component model from a plain Java code and user informations. Another is to read and interpret existing annotations. Another is to check the compatibility between one component model and an annoted Java code... On each iteration, the process accepts a java program (with or without annotation) and a component model (possibly empty). It computes some information, sometimes using external tools and human interaction. This information modifies the a java program and the component model.

The idea is to combine primitive transformations and develop a customised (or human driven) process B. Here are some a these primitive transformations:
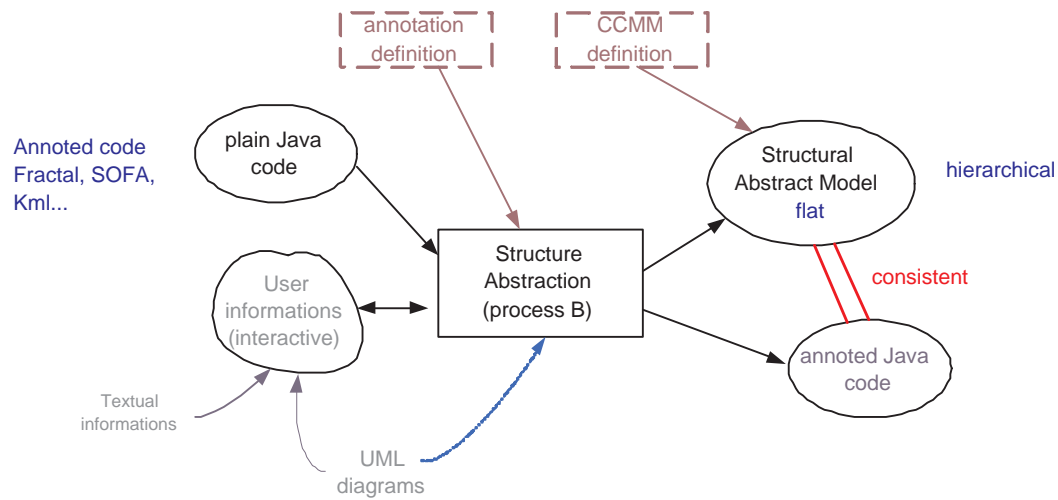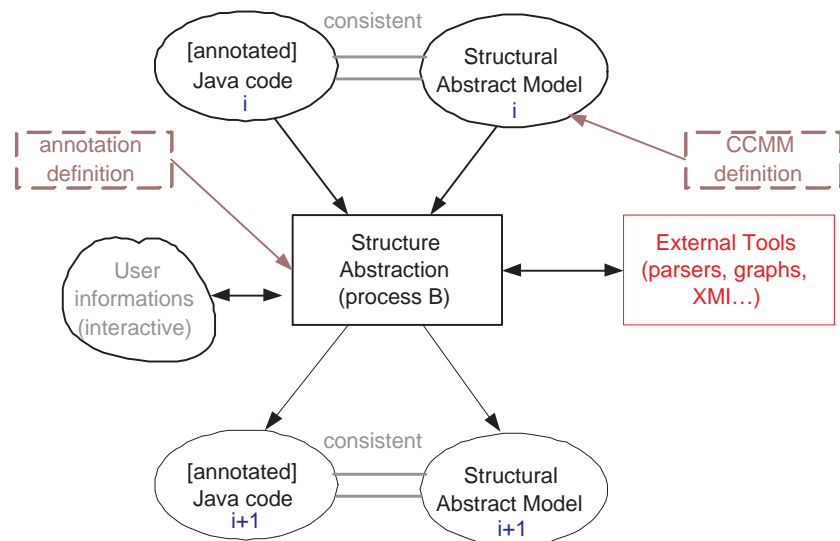
41

Figure 3.1: A general view of the process B

Figure 3.2: An iterative view of the process B

1. Annotate a Java program from user information.

2. Build a component model from an annotated Java source.

3. Build a component model from a plain Java source.

4. Analyse a distributed program to detect components (deployment).

5. Extract cluster using graph tools (grouping class into components, or grouping components into composite).

6. Process model transformations such as fusion, selection... on the couple (code, model).

7. ...

Important remarks:

1. Note that combining transformation 1 and 2 provides a first result of process B which can be reusable in process A.

2. Note also that input and outputs need format filters (reader, writer) which are common to all subprojects.

3. Note also that some of these transformations ought to be used is the other subprojects.

Consequently, process B is rather a tool box or a sequence of subprocess applications.
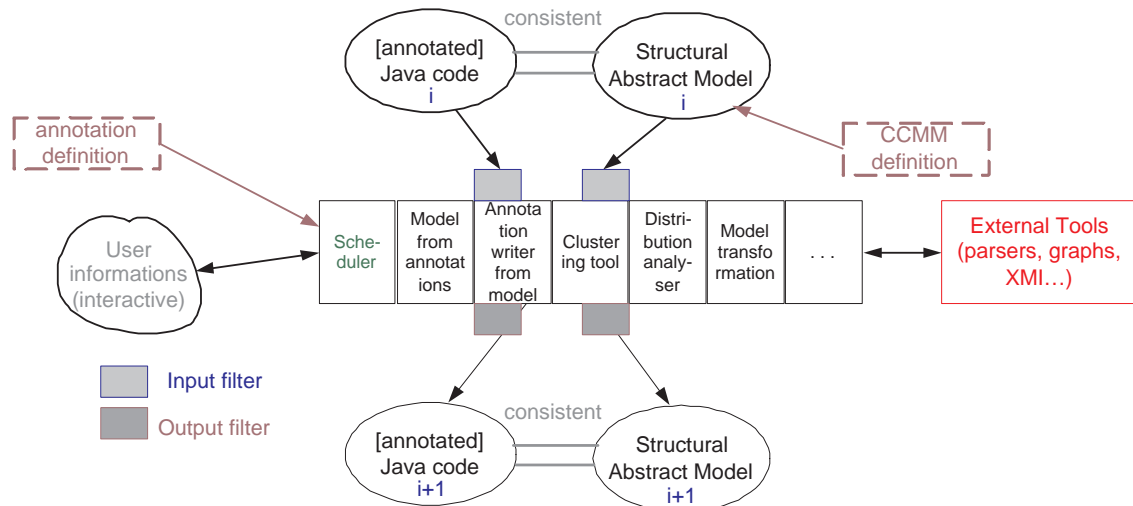

Figure 3.3: A architectural view of the process B

3.1.2 B transformations and tools

1. Annotate a Java program from user information.
    This program needs input/output functions for annotating Java sources.
    Some tools are

    - Java parsers, analysers... see section 3.2.3 on page 47.
    - JDK 5.0 Java Annotation Processing Tool APT[1].
    - A program that lead the interactions.
    -

2. Build a component model from an annotated Java source.
    Having an annotated Java program, one can build the corresponding model providing we have the good filters and formats (see the adapted transformations).

3. Build a component model from a plain Java source.
    This can be obtained by combining other transformations. Since the input model is empty, the human must provide many informations and can be helped by the cluster tool.

4. Analyse a distributed program to detect components (deployment).
    One way to find components is to analyse the distribution framework. Components in this case are linked to deployment nodes. We can use RMI analysis for example (or corba ?).

5. Extract clusters (grouping class into components, or grouping components into composite).
    We need graph tools to analyse component architectures.

6. Process model transformations such as fusion, selection... on the couple (code, model).
    In collaboration with the team working on the metamodel we have to develop transformations on models and their pending java annotation transformations.

7. Consistency checker.
    In collaboration with the team working on the metamodel we have to develop tools that check teh consistency between models and their corresponding annotated java programs.

---
[1] java.sun.com/j2se/1.5.0/docs/guide/apt/

8. Filters.
   In collaboration with the other teams we have to define the formats and to develop utilitary programs to read and write on the adopted format (XMI, MOF-XMI, Ecore, Java Model API, ...).

9. Scheduler.
   This program will chain the transformation in order to build interactive B processes.

3.1.3   Interface

The annotation language and component metal model have been discussed but the full definition of interface need an interoperable format.

1. Metamodel format (XMI, ECore, MOF, model API...)

2. Model format (XMI, ECore, MOF, model API...)

3. ...

   This work has to be done.

3.1.4   Organisation

This task is led by the COLOSS group; the OBASCO group also contribute significantly to the toolbox; the LCI team will bring its experience on reverse-engineering tools.

   The program is designed as a set of tools which can be developped independently provide the interface are well defined (see section 3.1.3). The list of tools is open and will be extended each time we need another tool.

   We have to distribute the transformations on the participants and to define which transformations are to include in each delivery. This work has to be done.

   First results on the structural analysis tool are expected by the time of the second workshop (Nantes 2008). Results on extraction back-ends are expected till the third workshop (Cluj 2008).

3.2   Behavioural Abstraction Subproject

Writer: Tomas Poch

   Reverse engineering general Java application into component application consists of two tasks. First, extraction of an architectural view (identification of components, their interconnections, etc.); second, extraction of a dynamic behavior specification of the components identified during the first task. Constituting an interface between the two tasks, the architectural information is to be stored in a form of Java annotation in the actual Java sources of the application being reverse engineered.

   This section presents goals, means and organization of the second task in the scope of the ECONET project, and thus summarizing a part of the results of the Prague'2007 Workshop working sessions.

3.2.1   Goals

Each group participating in the project has developed its own formalism for behavior specification. Therefore, the idea is to make the reverse engineering as general as possible in order to allow extraction of behavior in any formalism.

   To be more specific, the formalisms considered are:

- *Enhanced behavior protocols* (EBP) developed by DSRG

- *eLTS* developed by COLOSS

- *STS* developed by OBASCO

The individual behavior specification formalisms differ a lot. This makes creation of a general tool very difficult. However, steps common to extraction of any behavior specifications (in particular Behavior protocols and LTS-based formalisms eLTS and STS) might be identified. Thus, the general approach is to divide all necessary steps of behavior extraction into two parts: i) steps common to all formalisms, and ii) steps specific to a particular formalism.

The first part will be implemented in a General analysis tool, while the second part will be performed by back-ends specific to a particular formalism.

To prevent reinvention of the wheel, the analysis tool is to be implemented using existing libraries/tools/platforms (for parsing Java sources and annotation extraction, etc.).

To sum it up, the goals of reverse engineering behavior specification are as follows:

1. Find a suitable libraries/tools/platforms for analysis of Java sources

2. Create a generic Java analysis tool which produces an intermediate representation of behavior suitable for subsequent creation of concrete behavior specifications in a chosen formalism

3. Create formalism-specific back-ends for extraction of behavior specification from the intermediate specification

3.2.2 Annotations

Reflecting the goals stated in the previous section, the process of reverse engineering behavior specification starts, where the first process (reverse engineering component architecture) ends—i.e. by extracting and using architectural information provided in a form of Java annotations directly in the Java sources and, of course, the Java sources themselves.

The necessary architectural information consists of:

1. assignment of Java classes to individual components,

2. identification of provided interfaces and their methods,

3. identification of required interfaces (as class attributes), and

4. demarkation of the components' initialization code.

Moreover, in order to help extraction of behavior specification, it might be helpful to have explicitly annotated *ValueType* classes. By a ValueType, we mean a class used for storing and passing data rather than providing a specific functionality.

As the ValueTypes have to be reflected in the behavior specification, hints about the abstraction may be necessary. By abstraction we mean replacement of the actual ValueType by a simpler abstract type (for example integer type by enumeration). Although the abstract type has less states, it captures all information necessary for the component behavior. For example if the behavior of the component depends on the sign of the integer parameter, all information we need can be stored in the single bit of information. Thus, the boolan type suffices for the parameter.

The information about ValueType abstraction could be specific to the target formalism and even more added manually by a human user. More detailed description of the proposed set of annotations follows.

Component

One or more Java classes can be assigned to a single component. Such an assignment is specified by the following annotation of a Java class:

```
@inComponent(annotation_src, component_name)
```

As in the rest of this section, the `annotation_src` specifies the origin of the annotation—i.e. a name of a tool which inserted the annotation in the code or whether the annotation has been inserted manually.

Interfaces

Methods of Java classes may be assigned to component's *provided interfaces* in two different ways. First, as an annotation of a Java class:

```
@provided(annotation_src, model_iface_name, java_iface_name)
```

This way, all methods of the specified Java interface (which the annotated class has to implement) are marked as a part of the provided interface of the component. The second possibility is to mark individual methods of a Java class by the annotation:

```
@provided(annotation_src, model_iface_name)
```

This is necessary in a case the component's provided interfaces do not correspond with the Java interfaces.

In Java sources, a required interface is present in a form of a class attribute. The attribute stores a reference to another component, whose provided interface is bound to this required interfaces. Therefore, the target of the annotation for required interface is an attribute of a Java class:

```
@required(annotation_src, model_iface_name)
```

Component's initialization

In order to be able to derive the behavior specification correctly, knowledge about a component initialization is crucial. By initialization, we mean instantiation of classes, assigning references to the required interfaces (i.e. to the class attributes), starting threads of active components, etc.

Typically, there are two ways of initialization. First, the initialization is performed by a class. This class is the first instantiated and is responsible (its constructor) for the instantiation and initialization of the component's content. To identify such a class, the following annotation is used:

```
@initclass(annotation_src, component_name)
```

The second possibility is that the component content is instantiated and initialized by a static method (an equivalent of the *main* method).

```
@initmethod(annotation_src, name_of_the_component)
```

Data and method parameters abstraction

Some of the internal component data and method parameters might be of crucial importance for correct extraction of the behavior specification. Those should be also explicitly annotated, in order that the behavior specification extractor could take them into account and provide a necessary abstraction.

However, it is hard to predict the needs of behavior extraction in advance and thus these annotations are yet to be further elaborated. The initial proposal is to identify ValueTypes classes and interfaces, and therefore mark all their instances as important for the component behavior.

```
@businessparametertype(annotation_src)
```

As some types might be important only in a specific context (e.g. String), there is also possibility to mark particular method parameters and Java class attributes as important for business logic.

```
@businessattribute(annotation_src)
@businessparameter(annotation_src, parameter_name)
```

For the actual behavior specification extraction, hints on how to perform a data abstraction from the concrete values of these types have to be added by a human user. A particular format of these hints will be further analyzed.

External dependencies

Some architecture information cannot be introduced into the code as annotations. Mostly, these are the external dependencies on the class libraries used by the component code. Classes from the libraries can be also important for the behavior extraction but in general, they cannot be directly annotated. Therefore, these dependencies are listed in an extra file. The complete code of the component is thus a set of classes annotated by both the `InComponent` annotations and also the content of the dependencies file.

3.2.3 Tools for Java source analysis

Having the Java sources properly annotated, the question of how to extract the annotations and analyze the sources comes up. There is quite a choice of tools to be used for this purpose.

Possible options are:

- JavaC [2]—standard Java compiler from Sun—is a natural first option as it is standard part of the Java development kit (JDK) and features a reasonable interfaces for either annotation processing alone or to obtain the complete abstract syntax trees.

- JavaCC (Java Compiler Compiler) [1] is a generator of parsers. To create a parser, it uses a LL(n) grammar.

- ANTLR [3] is another parser generator which also uses LL(n) grammars.

- Java CUP [4] is also a parser generator, but in comparison to the previous ones it uses LALR(1) grammars. It is quit similar to the standard YACC and Bison tools. In contrast, it is written in Java.

- SableCC [5] is another LALR(1) parser generator.

In a case, the chosen parser generator does not provide a lexical analyser, a usage of tools like JLex and JFlex has to be considered.

Choosing the suitable tool will require deeper exploration and in-depth analysis of all features provided by the tools. The preferred option is to use JavaC, as it allways guarantees to parse the current (and also older) version of the Java languages and also it does not introduce any third-party tool dependencies.

3.2.4 Generic analysis tool (GAT)

Behavior specification extraction is divided into two steps. First step is to preprocess the annotated Java sources and propagate information about architecture to the actual Java code, so that Java statements can be categorized as either externaly visible events (method calls on required interfaces), visible actions (change of business attributes), changes in control flow (if and cycle statements), or internal invisible actions (work with local variables). The result of the process is an abstract syntax tree with nodes "colored" by the architecture information (obtained from the annotations).

In the second step, the produced cAST (colored AST) is used to create a control flow graph (CFG) representing the code. Again, nodes of the graph are "colored" by the architecture information (cCFG). Both cAST and cCFG is available by the defined API (all details about the API have to be analyzed yet) and/or in a serialized textual form.

A specific formalism back-end can choose whether cAST is sufficient for it or whether the usage of cCFG is necessary.

Based on the preliminary analysis, for the EBP back-end cAST is sufficient while the LTS-based formalism (eLTS and STS) requires an existence of cCFG.

The whole process is illustrated on Figure 3.4.

There is a bigger set of transformations which can be performed among AST, cAST, cCFG. Some of them, like coloring, are necessary part of the proposed process. Other transformations, like ommiting actions on non-ValueType data, can be used by some back-ends or even the single one. These transformations should be available to the authors of the back-ends in order to prevent duplicit work.

3.2.5 Reverse engineering back-ends

Depending on the choice of the target behavior specification formalism (BP, EBP, STS, eLTS, ...) a specific back-end of GAT is used to extract the behavior specification. For example, the back-end for extraction of behavior protocols abstracts from everything except externaly visible events and control flow (replacing `if` statements by alternative and cycles by repetition).
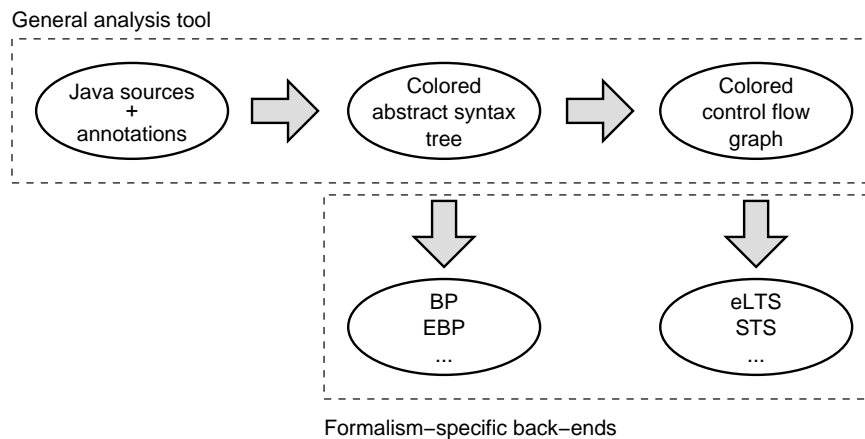
General analysis tool

Formalism−specific back−ends

Figure 3.4: The two step process of reverse engineering behavior specification

3.2.6 Organization

This task is led by the DSRG group; the OBASCO and COLOSS groups also participate. The first version of the set of annotations was created by all groups during the Prague 2007 Workshop. However, the initial version is expected to be further enhanced in order to allow automated (or at least semi-automated) abstraction of ValueTypes.

More specifically, DSRG is responsible for creation of the general analysis tool. For this tool, each participating group (DSRG, OBASCO, COLOSS) is going to implement their own back-ends specific to the behavior specification formalisms they use.

First results on the generic analysis tool are expected by the time of the second workshop (Nantes 2008). Results on extraction back-ends are expected till the third workshop (Cluj 2008).

3.3 Metamodel Abstraction Subproject

Writer: Dan Chiorean

NB: *currently in romanian, put in a latex format by pascal, it will be translated soon.*

3.3.1 Objectives and Goals

3.3.2 Participants

3.3.3 Means

3.3.4 Tasks and Schedule

3.3.5

Utilizarea asertiunilor în modelare - vazuta din perspectiva evaluarii lor

În cadrul acestei lucrari, vom analiza modul în care specificarea asertiunilor în modelare este influentata de felul în care se realizeaza evaluarea lor: static, dupa construirea modelului sau dinamic, în timpul construirii si modificarii lui. Vom încerca sa evidentiem unele aspecte sensibile de care trebuie sa tinem seama în cazul unor astfel de specificari, chiar si în cazul unor situatii simple.

Pentru a exemplifica afirmatiile noastre, vom considera un model relativ simplu, si anume metamodelul Core-Components, care grupeaza trasaturile comune ale majoritatii limbajelor de modelare a componentelor [BuresH-netynkaPlasil]. Pentru o lizibilitate mai mare, în figura 1 sunt prezentate numai elementele metamodelului referite în cadrul asertiunilor analizate în continuare.
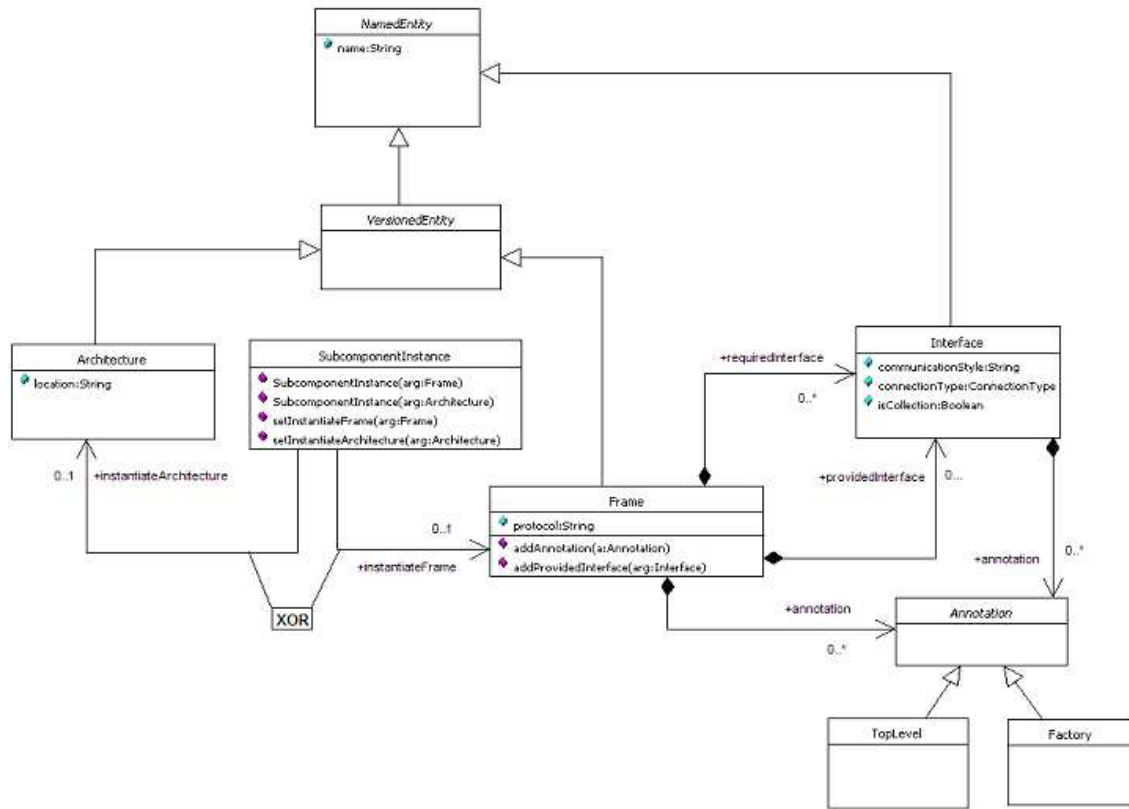
Figure 3.5: A part of the CoreComponent Metamodel

Validarea unui model, poate fi privita din doua puncte de vedere diferite, care, asa cum vom prezenta în continuare, influenteaza modul de specificare a asertiunilor.

Validare statica - Constrângerile permit corectarea si validarea unui model deja construit

Aceste modele reprezinta instantieri ale unui metamodel, obiectivul nostru fiind sa verificam daca toate constrângerile asociate metamodelului sunt satisfacute de modelele care urmeaza a fi validate. Problema este perfect similara celei în care un model UML construit cu ajutorul unui instrument adecvat: Rational Rose, Poseidon, MagicDraw etc. este validat în raport cu WFR-urile specificate pentru metamodelul UML. În acest caz, constrângerile sunt exprimate prin intermediul invariantilor.

Asa cum se mentioneaza în diverse lucrari [x], pe parcursul construirii modelului, acesta nu este complet si în unele cazuri este chiar incorect din punctul de vedere al constrângerilor limbajului de modelare. De aceea, înainte de realizarea unor transformari ale modelului, este important sa verificam corectitudinea si completitudinea lui (compilabilitatea modelului).

Pentru metamodelul CoreComponents, constrângerea specificata grafic între asocierile unidirectionale spre Frame, respectiv Architecture se poate exprima simplu sub forma urmatorului invariant:

```
context SubcomponentInstance
   inv FrameOrArchitectureAssoc:
      self.instantiateArchitecture.isUndefined xor self.instantiateFrame.isUndefined
```

În cazul în care invariantului este evaluat la false, simpla evaluare a celor 2 subexpresii ale lui xor permite identificarea cauzei eorii, permitând modelatorului sa remedieze eroarea.

În cazul constrângerii referitoare la unicitatea numelui pentru requiredInterfaces atasate unui Frame, invariantul:

```
context Frame
   inv requiredInterfacesName:
      self.requiredInterface.name->isUnique(n | n)
```
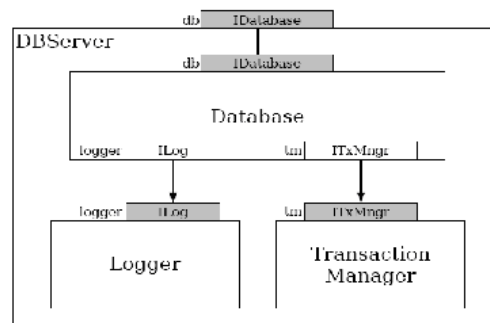
Figure 3.6: Components with provided and required Interfaces

nu sprijina suficient identificarea interfetelor care au determinat violarea invariantului. Prin evaluarea numelor tuturor interfetelor, putem, dupa cautari repetate, sa identificam interfetele care au condus la violarea invariantului. În cazul în care numarul acestor interfete este mare, cautarea consuma timp si este neplacuta O specificare mai potrivita pentru acest obiectiv (care permite identificarea rapida a interfetelor cu acelasi nume) este:

```
context Frame
   inv requiredInterfacesName:
      let ri = self.requiredInterface in
      (ri->reject(e | ri.name->count(e.name)=1))->isEmpty
```

În cazul violarii invariantului, evaluarea colectiei de interfete ale caror nume nu sunt unice, ne permite sa parcurgem simplu (prin navigare - în cazul utilizarii OCLE) interfetele care au violat invariantul si sa corectam modelul. În cazul în care conditia de unicitate se refera atât la interfetele required cât si la cele provided, specificarea este:

```
context Frame
   inv uniqueInterfacesName:
      let i = self.requiredInterface->union(self.providedInterface) in
      (ri->reject(e | ri.name->count(e.name)=1))->isEmpty
```

[Imagine cu snapshot OCLE si explicatiile suplimentare - la fel si mai sus cand am nume]

Pentru constrângerea referitoare la tipul elementelor ce pot fi asociate ca adnotari ale lui Frame, vom adopta o solutie similara celei precedente:

```
context Frame
   inv annotations_Type:
      self.annotation->select(e | not e.oclIsTypeOf(TopLevel))->isEmpty
```

Evaluare a colectiei rezultata în urma operatiei select, va permite identificarea adnotarilor care nu satisfac constrângerea.

Constrângeri care sprijina pastrarea validitatii modelului dupa fiecare operatie - Validare dinamica

Aceasta situatie este similara celei în care avem o aplicatie orientata-obiect, aplicatie în care obiectele sunt create, distruse sau îsi schimba starea. Universul obiectelor trebuie sa fie în permanenta valid. În consecinta, trebuie sa luam masuri adecvate pentru ca în urma fiecarui apel de constructor sau modificator care poate schimba starea sistemului, aceasta sa ramâna valida. Politica adoptata va fi în conformitate cu proverbul "better prevent than cure", în consecinta, constrângerile vor fi specificate preponderent cu ajutorul (folosind) preconditiilor si postconditiilor.

Ca si în cazul verificarii statice, vom analiza mai întâi constrângerea dintre cele doua asocieri unidirectionale care pleaca din clasa SubcomponentInstance spre clasele Frame respectiv Architecture, fiecare având multiplicitatea 0..1 pentru capetele de asociere navigabile. Constrângerea XOR între cele 2 asocieri, exprimata grafic în diagrama de clase, semnifica faptul ca dinspre SubcomponentInstance vom avea întotdeauna o asociere spre una din clasele destinatie Frame sau Architecture si numai una.

În conformitate cu principiile "design by contract" [Meyer], invariantul trebuie sa fie satisfacut pe întreaga durata de viata a obiectului, exceptând momentele în care asupra lui sunt aplicati modificatori. O prima consecinta este ca, întotdeauna obiectele de tip SubcomponentInstance vor trebui produse folosind constructori expliciti. În cazul în care vom avea un singur astfel de constructor, printre parametrii sai va trebui sa avem câte o referinta spre Frame respectiv Architecture. Doar una dintre aceste referinte va trebui sa fie valida, cealalta trebuind sa fie null (undefined). O solutie este:

```
context SubcomponentInstance::SubcomponentInstance(a:Architecture,
                                          f:Frame):SubcomponentInstance
   pre subcomponentInstance_connection:
      (a.isUndefined and f.oclIsTypeOf(Frame)) or
      (f.isUndefined and a.oclIsTypeOf(Architecture))
   post subcomponentInstance_connection_:
      (result.instantiateFrame = f) and (result.instantiateArchitecture = a)
```

Conform filosofiei OOP, apelul uneia dintre metodele setInstantiateFrame(f:Frame), respectiv setInstantiateArchitecture(a:Architecture) trebuie sa poata modifica numai referinta transmisa ca parametru. În consecinta, pentru acesti modificatori preconditiile vor fi:

```
context SubcomponentInstance::setInstantiateFrame(f:Frame)
   pre instantiate_Frame:
      self.instantiateArchitecture.isUndefined and f.oclIsTypeOf(Frame)
   post instantiate_Frame:
      self.instantiateFrame = f

context SubcomponentInstance::setInstantiateArchitecture (a: Architecture)
   pre instantiate_Architecture:
      self.instantiateFrame.isUndefined and a.oclIsTypeOf(Architecture)
   post instantiate_Architecture_:
      self.instantiate Architecture = a
```

În cazul în care se doreste ca un obiect SubcomponentInstance sa-si poata schimba unica asociere, dinspre un Frame spre un Architecture sau invers, aceasta shimbare va determina modificarea ambelor referinte, atât cea spre Frame, cât si cea spre Architecture. În consecinta, pentru o astfel de operatie trebui construita o noua metoda, având acceasi preconditie ca si cea prezentata mai sus pentru constructorul explicit. În corpul acestei metode va fi apelata metoda set corespunzatoare, dupa ce în prealabil referintele spre Frame respectiv Architecture vor fi setate la null.

Pentru constrângerea referitoare la unicitatea numelui unei requiredInterface atasate unui Frame, vom avea în vedere ca aceasta cerinta poate fi modificata numai la adaugarea unei noi interfete, în consecinta preconditia corespunzatoare va fi atasata operatiei addRequiredInterface(i:Interface):

```
context Frame::addRequiredInterface(i:Interface)
   pre uniqueName:
       i.name.size > 0 and self.requiredInterface.name->excludes(i.name)
   post uniqueName_:
      self.requiredInterface->size = self.requiredInterface@pre->size + 1 and
      self.requiredInterface.name->includes(i.name)
```

Daca luam în considerare posibilitatea schimbarii numelui unei interfete dupa ce a fost atasata la un Frame, atunci, va trebui sa specificam o preconditie adecvata pentru metoda Interface::setName(n: String).

În cazul în care exista constructori expliciti ai clasei Frame care seteaza legaturi spre Interface pentru rolul requiredInterface, atunci va trebui verificata constrângerea asociata numelui în cadrul respectivilor constructori.

Ultima constrângere se refera la tipul elementelor ce pot fi atasate ca anotari ale lui Frame. Si în acest caz, asertiunea va fi specificata cu ajutorul unei preconditii.

```
context Frame
context Frame::addAnnotation(a:Annotation)
   pre param_s_type:
      a.oclIsKindOf(TopLevel)
```

ů Asertiunile nu trebuie sa fie nici prea constrictive, nici prea permisive (ele trebuie sa exprime fidel conditiile ce trebuie indeplinite pentru un comportament predictibil si corect al obiectelor unei aplicatii). De aceea evaluarea asertiunilor OCL pe un numar semnificativ de cazuri, trebuie sa conduca fie la validarea constrângerilor specificate informal (în limbaj natural) sau la corectarea lor. În cadrul specificarii riguroase a constrângerilor într-un limbaj formal si al evaluarii lor, trebuie sa raspundem la o serie de întrebari care sa ne permita sa întelegem mai bine si mai exact rolul constrângerilor si comportamentul aplicatiei. ů Asertiunile trebuie specificate într-un mod expresiv si cât mai simplu cu putinta. Evaluarea lor trebuie sa fie cât mai simpla si daca se poate, sa fie realizata numai atunci când este nevoie. ů În cazul specificarii invariantilor, trebuie sa avem în vedere momentele evaluarii lor. Evaluarea realizata dupa fiecare apel de constructor este obligatorie asa cum mentioneaza foarte clar Meyer [OOSC]. Pentru a evita efectuarea unor operatii redundante, invariantii vor trebui evaluati numai dupa apelul modificatorilor care ar putea altera starea obiectului, nu dupa apelul fiecarui modificator.

3.4   Common Tools

Several tools will be used in more than one subproject.

1. JavaCC, https://javacc.dev.java.net/

2. Java Development Kit, http://java.sun.com/

3. ANTLR, http://www.antlr.org/

4. Java CUP, http://www2.cs.tum.edu/projects/cup/

5. SableCC, http://sablecc.org/

Chapter 4

Conclusion

We report many informations of the report in this document. This work has also been achieved to be the technical part of the project first year report.

The workshop emphasis the (intuited) fact that the abstract models of the partners share a common basis on components, services and behaviours. The differences can be seen merely as enrichment rather than concurrency. A common meta-model can therefore be proposed, which can be augmented later to be a proposal for component model interoperability. The cross fertilisation seems also possible at the tool level.

A plan is sketch for a first step proposal in component abstraction from java code. We fixed a limited context and objectives to be achieved in one year and several monthes. The practical implementation will be led in the second year.

Appendix A

More informations on...

see the Project Wiki.

A.1 Workshop Material

Most of the elements are on the project and workshop Wiki.

A.2 Collaborative Tools

Some collaborative tools have been installed to exchange documents.

- Wiki
  http://www.lina.sciences.univ-nantes.fr/coloss/wiki/doku.php?id=econet:start
  For storing documents, discussions...

- CVS
  http://www.ksi.mff.cuni.cz/??cvs??/

- CoCoME
  http://agrausch.informatik.uni-kl.de/CoCoME
  http://www.cocome.org/

A.3 Annotated Bibliography

We summarise some useful papers on the subject. These papers can be downloaded on the wiki, project material.

A.3.1 General Papers

- Reverse Engineering: A Roadmap Hausi Müller at al. [MJS$^+$00]

- Pattern-Based Reverse-Engineering of Design Components Rudolf K. Keller et al. [KSRP99]

- The Vienna Component Framework Enabling Composition Across Component Models Johann Oberleitner et al. [OGJ03]

- A technique for automatic component extraction from object-oriented programs by refactoring Hironori Washizakia et al. [WF05]

- Program and interface slicing for reverse engineering Jon Beck et al. [BE93]

- A Simple Method for Extracting Models from Protocol Code David Lie et al.[LCED01]

- An Intermediate Representation for Integrating Reverse Engineering Analyses Rainer Koschke, et al. [KGW98]

A.3.2 Java Reverse Engineering

- Experiences with the Development of a Reverse Engineering Tool for UML Sequence Diagrams: A Case Study in Modern Java Development Matthias Merdes et al. [MD06]

- Reverse Engineering a Large Component-based Software Product JM Favre and al. [FED$^+$01]

A.3.3 Code Model Checking

- Model-checking Distributed Components: The Vercors Platform Tomas Barros1 at al.[BCMR07]

A.3.4 Trace Exploration

- A Survey of Trace Exploration Tools and Techniques Abdelwahab Hamou-Lhadj et al. [HLL04]

- Bandera: extracting finite-state models from Java source code Corbett, J.C. et al. [CDH$^+$00]

- Tool-supported program abstraction for finite-state verification Dwyer, M.B. et al. [DHJ$^+$01]

- Component Recovery, Protocol Recovery and Validation in Bauhaus Thomas Eisenbarth et al. [EKV05]

A.3.5 Members publications on the subject

DSRG

- Runtime Support for Advanced Component Concepts Tomas Bures et al. [BHP$^+$07]

- Modeling Environment for Component Model Checking from Hierarchical Architecture Pavel Parizeka et al. [PP07a]

- Specification and Generation of Environment for Model Checking of Software Components Pavel Parizek et al. [PP07b]

- Model Checking of Component Behavior Specification: A Real Life Experience Pavel Jezek et al. [JKP06]

- Model Checking of Software Components: Combining Java PathFinder and Behavior Protocol Model Checker Parízek Pavel, et al. [PPK07]

- Model Checking of Software Components: Making Java PathFinder Cooperate with Behavior Protocol Checker Parízek Pavel, et al. [PPK06]

OBASCO

- Java Implementation of a Component Model with Explicit Symbolic Protocols Sebastian Pavel et al. [PNPR05]

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[AAA06]   Christian Attiogbé, Pascal André, and Gilles Ardourel. Checking Component Composability. In *5th International Symposium on Software Composition*, volume 4089 of *Lecture Notes in Computer Science*. Springer Verlag, 2006.

[BCMR07]   Tomás Barros, Antonio Cansado, Eric Madelaine, and Marcela Rivera. Model-checking distributed components: The vercors platform. *Electron. Notes Theor. Comput. Sci.*, 182:3–16, 2007.

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