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Components with N-Party Rendezvous and Symbolic Transition Systems

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- Component based software engineering: To get a formal and executable model
- Explicit protocols integrated into component interfaces to describe their behaviour in a formal way
- Explicit protocols are often dissociated from component code
- Generally only binary synchronisation are provided
- Computing more than flat behavioural models
- Fill the gap between high-level formal models and implementation of protocols
- Formal analysis methods to verify components and their interactions
- Tool support: an API with parsers, and some analysis tools

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The Lamport Algorithm

One server and several processes

- The server has to manage mutual exclusion of the processes
- Here two processes to simplify
- Several variants which use integer (bounded or not) to control critical section
- Ensures mutual exclusion, deadlock freeness, but not fairness

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STS State Machine

• Symbolic Transition System = Dynamic Part + Data Part

- Dynamic Part: Guarded Input/Output finite state machine with actions
- Data Part: Might be provided as an ADT specification
- The ADT to Java translation has been tested but not integrated
- Data Part = a Java interface (optional) and a Java class

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The Process Java Class

public class Process extends Data {

```
//The local ticket.
public int A;
```

```
// Default constructor.
public Process() { this.A = 0; }
```

```
// Get a ticket.
public void think(int t) { this.A = t; }
```

```
// the check guard
public boolean check(int s)
        { return this.A == s; }
```

```
// Enter critical section.
public void use(int s) { }
```

```
// End section.
public void end() {}
```

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The server component



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An Architecture with Two processes



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Lamport Architecture

ROOT examples lamport PACKAGE examples.lamport

NEED Server:: Process::

LOCALS s:Server p1 p2 : Process

COMMUNICATIONS XOR s.givet p1.think p2.think COMMUNICATIONS XOR s.gives p1.use p2.use COMMUNICATIONS XOR s.end p1.end p2.end

BINDINGS s.givet p1.think -> give1 BINDINGS s.givet p2.think -> give2

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- Previous first communication line is equivalent to: COMMUNICATIONS AND s.givet p1.think COMMUNICATIONS AND s.givet p2.think
- It expresses two synchronisations (here binary rendezvous)
- N-party rendezvous (AND) can involve any number of participants
- All of them execute "synchronously" their actions
- It allows one way but multiple value exchanges
- Communication requires compatibility offer

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N-Party Rendezvous

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- Computing a flat behaviour is not sufficient for architectures
- Based on the synchronous product of LTS
- But need to consider guards, communications and actions
- Communications are synchronisation vectors
- Guards, events and actions are structured
- But it cannot takes into account a complex hierarchy with more than one level

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Synchronous Product



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Fairness Controller Architecture

NEED

the controller .sts
Controller: :
the inner Lamport.comp
Inner : Lamport :

LOCALS I: Inner c: Controller

COMMUNICATIONS AND c.one I.give1 COMMUNICATIONS AND c.other I.give2

no bindings

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The Resulting Behaviour



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• To synchronise complex state machines with guard, communication and actions

- To allow N-party rendezvous
- To keep the structure of the composite in the result
- To hide, export or rename events
- To allow various bindings connections:
 - What to export outside ?
 - What kind of connection are allowed ?
 - What are the communication rules ?
 - •

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- To allow N-party rendezvous
- To keep the structure of the composite in the result
- To hide, export or rename events
- To allow various bindings connections:
 - What to export outside ?
 - What kind of connection are allowed ?
 - What are the communication rules ?
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Event Interactions in Smart Home



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Smart Home Architecture

Internal Alarm assembly
NEED Light: :
 Siren: :
 AlarmControl: :
 PresenceDetector: :

LOCALS pd:PresenceDetector l:Light s:Siren a:AlarmControl

COMMUNICATIONS AND pd.intrusion a.intrusion COMMUNICATIONS AND a.onOff s.onOff COMMUNICATIONS AND a.switch I.switch

duplicated port BINDINGS pd.intrusion -> check # branch port BINDINGS pd.intrusion a.intrusion -> block

Merged Port Example



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DoorOpener

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DoorOpener definition
ROOT test fr emn stslib SH doorOpener
PACKAGE fr.emn.stslib.SH.doorOpener

NEED Detector: : Lock: :

LOCALS d: Detector I:Lock

COMMUNICATIONS AND d.open I.open

two simple ports
BINDINGS 1.unlock -> unlock
BINDINGS d.count -> count
two merged ports
BINDINGS d.disable 1.disable -> disable
BINDINGS d.enable 1.enable -> enable

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DoorOpener and Security

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DoorOpener + Security definition ROOT test fr emn stslib SH doorOpener PACKAGE fr.emn.stslib.SH.doorOpener

NEED DoorOpener: Security: :

LOCALS o: DoorOpener s:Security

COMMUNICATIONS AND o.enable s.off COMMUNICATIONS AND o.disable s.on

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- More sophisticated than usual synchronous product of automata
- Complex transitions with guard, communication offers and actions
- Keep inside states, transitions and other elements the structure of the system
- N-party rendezvous and complex bindings, and renaming
- Global semantics for a composite: synchronous product at each level + some additional computation to manage bindings and renaming

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Structured Product Algorithm

• Algorithm to compute the global semantics:

- if it is a simple STS then return it
- if it is a composite then
 - recursively compute the global semantics of each subcomponent,
 - update the synchronisation list to cope with duplicated and merged,
 - If connected outside left bindings are added as new synchronisations
 - · compute the synchronous product,
 - and rename the events according to bindings
- Need a notion of Nary STS

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Structured Product

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- A component is compatible with an assembly iff the composite is deadlock free
- Build a composite with the components, compute the structured product, and check for deadlocks
- Undecidable: It covers more than simple behavioural compatibility since STS are a general model of computation
- Decidable in case of bounded system or I/O STS components
- Problem: there is neither sufficient nor necessary criterion thus the property is difficult to check on real examples with guards

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- With the controller example (exports entering1 and s.end p1.end -> entering2): we cannot observe synchronisations with the first process but it is still compatible !
- *Event strict* architecture: at each level, each synchronisation declared in the COMMUNICATIONS clauses occurs at least once in the behaviour
- Checking undecidable but decidable with bounded system or I/O STS components
- Static checking: if the structured product is not event strict then the system is not event strict
- First check event strictness then compatibility

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Two Early Checking Properties

- A hierarchical component model based on STS, a N-party rendezvous and sophisticated protocols with guards
- Use of N-party rendezvous: required to control component behaviour keeping a black box approach
- Tools to compute the global protocol associated to assemblies and also to analyze and check syntactic and behavioural properties
- Necessary support to analyze the communications and the compatibility of components
- Turing complete notion as STS and N-party rendezvous: control any kind of components in a truly compositional way

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Conclusions

- A hierarchical component model based on STS, a N-party rendezvous and sophisticated protocols with guards
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Future work

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Implementation of GUI to allow edition and visualization of STS and composite

- More complete set of verifications, specific attention will be on abstraction methods and bisimulation
- Enrich our set of interactions and to assist the user in the choice and the use of these interactions

Future work

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